

Figure 1

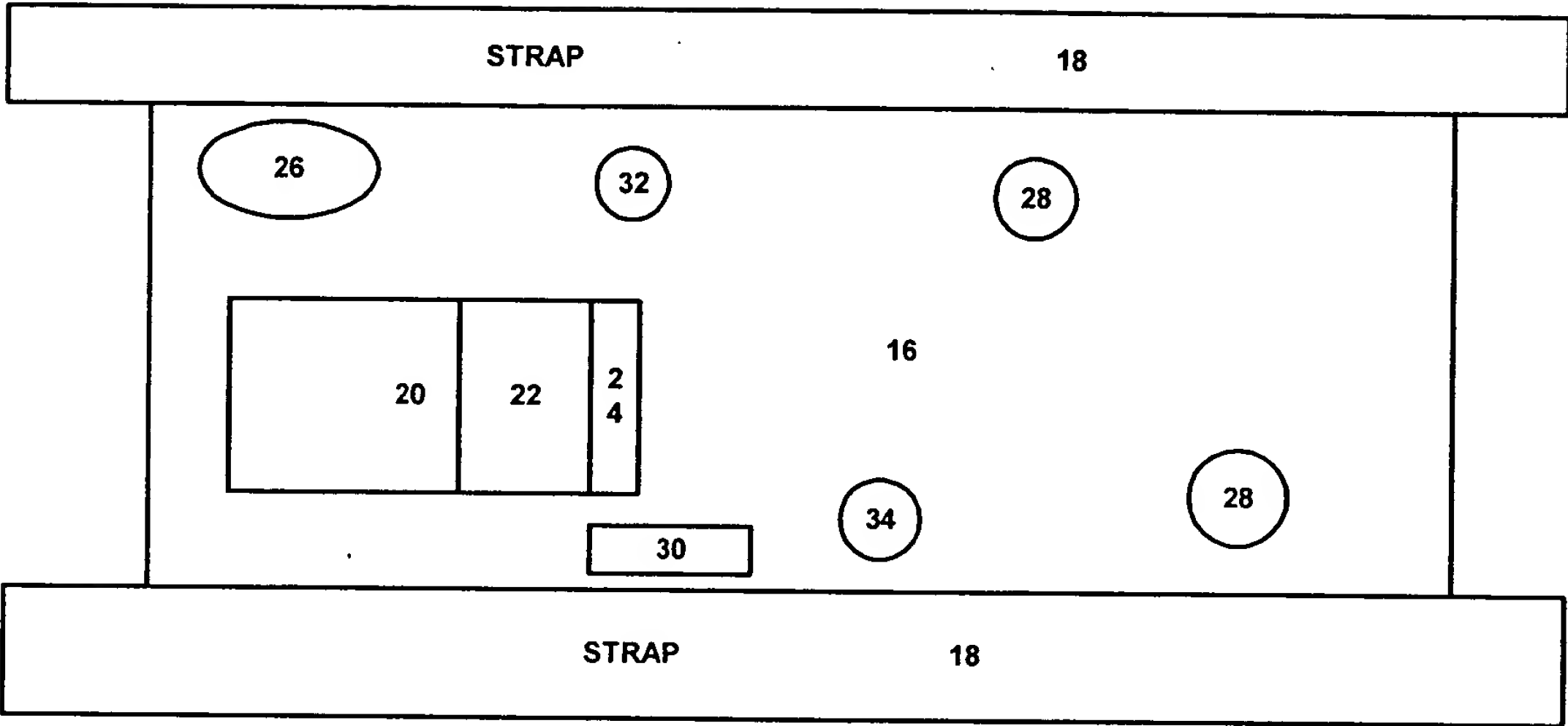


FIGURE 2

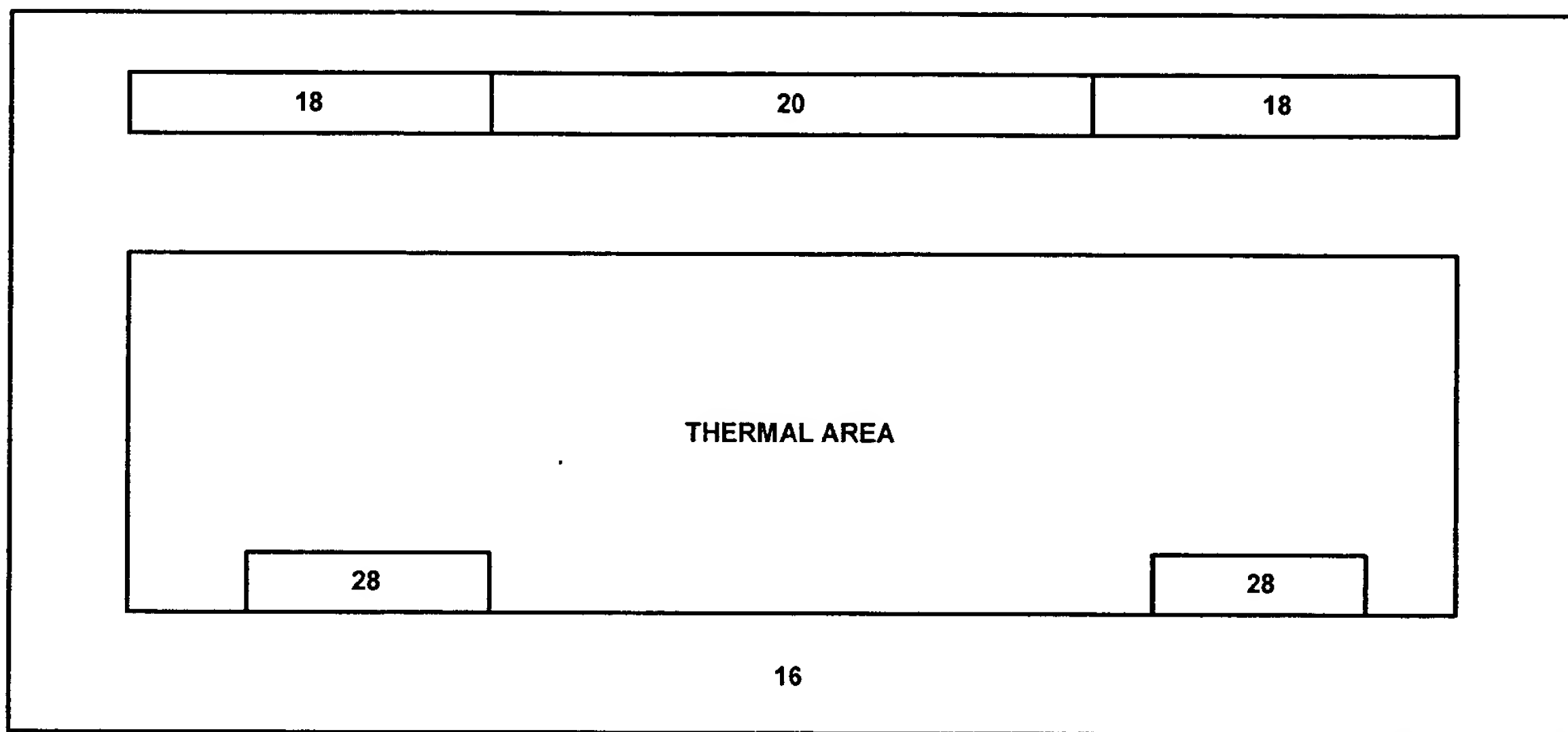


FIGURE 3

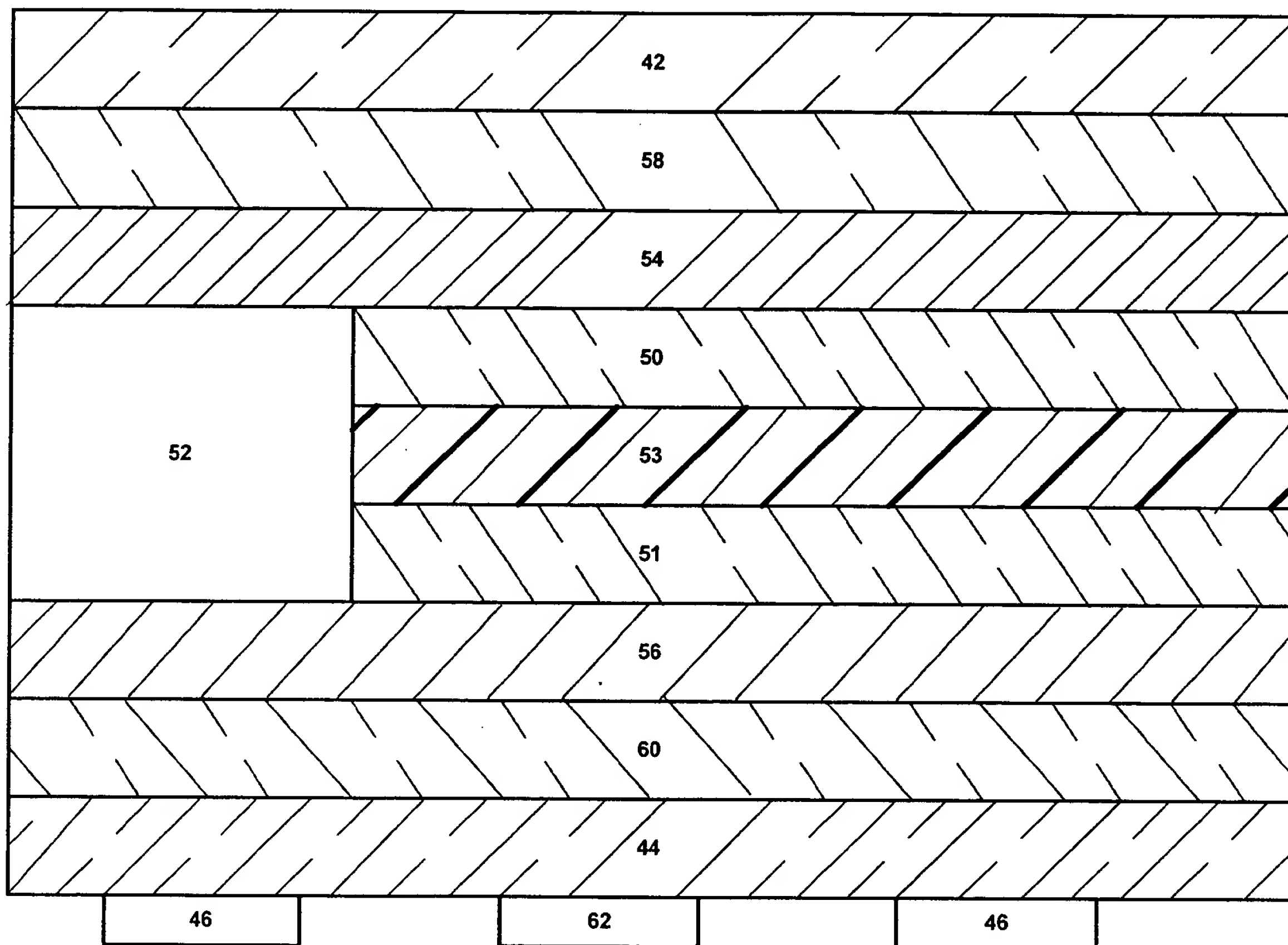


FIGURE 4A

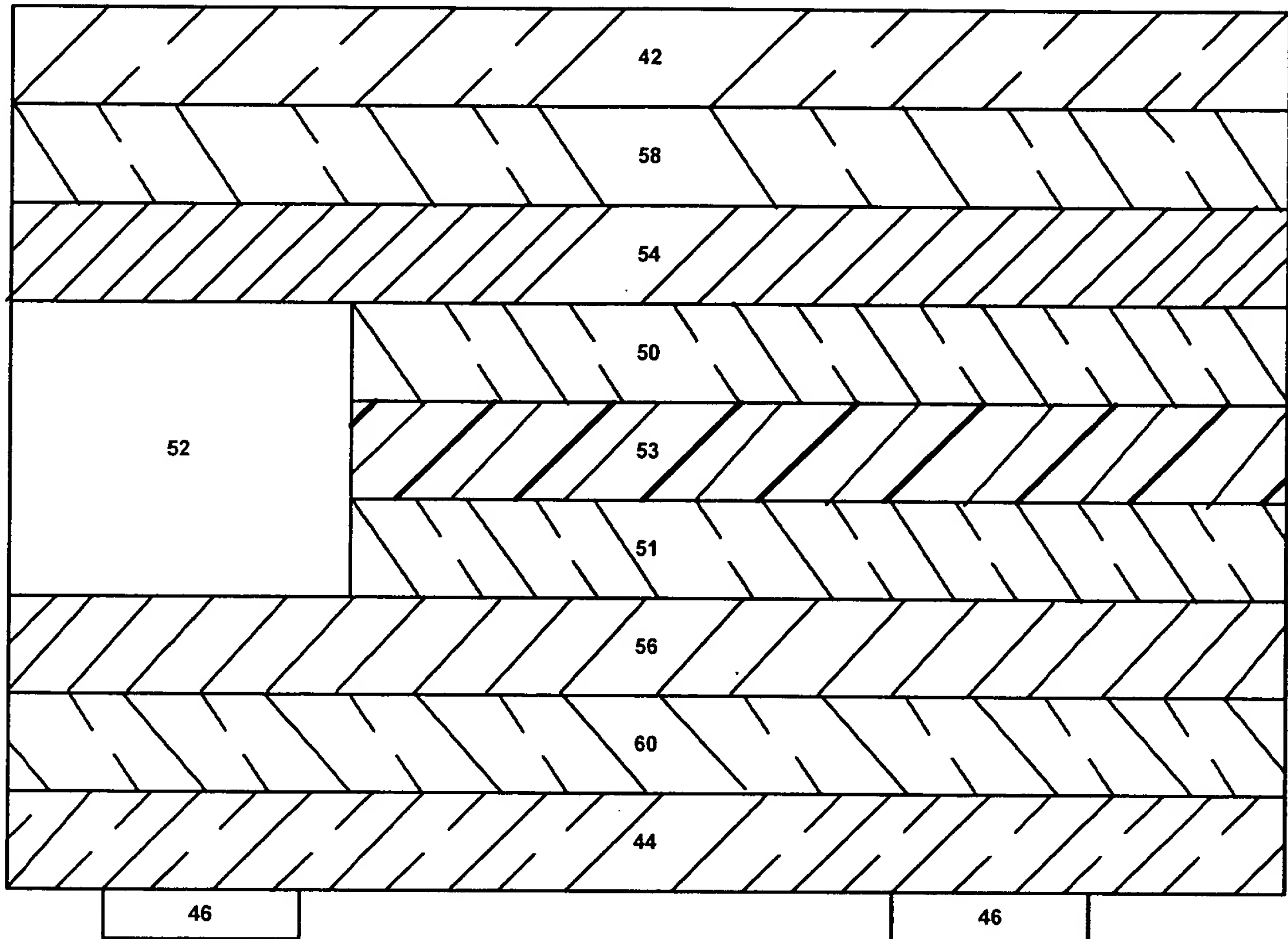


FIGURE 4B

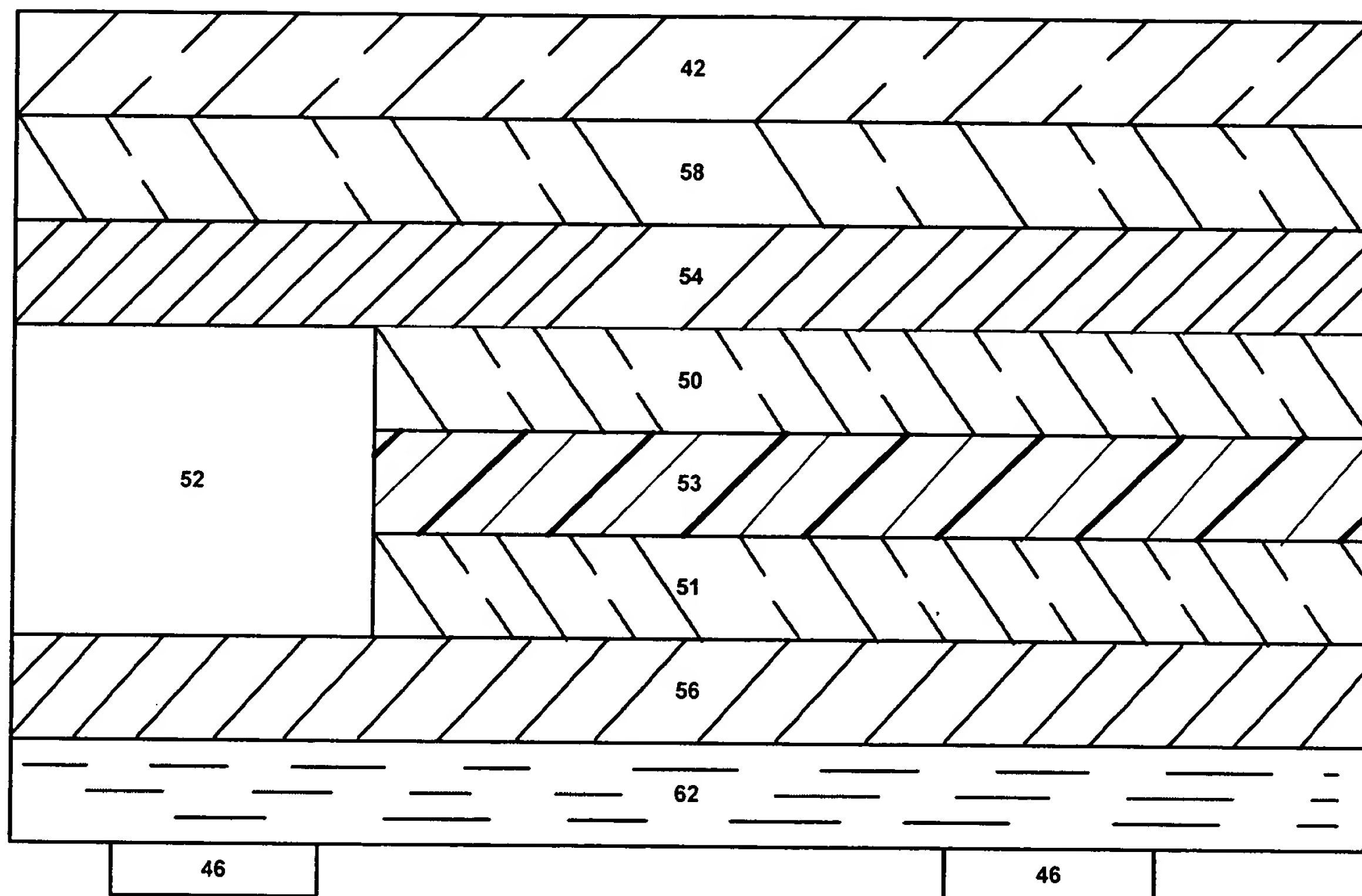


FIGURE 4C

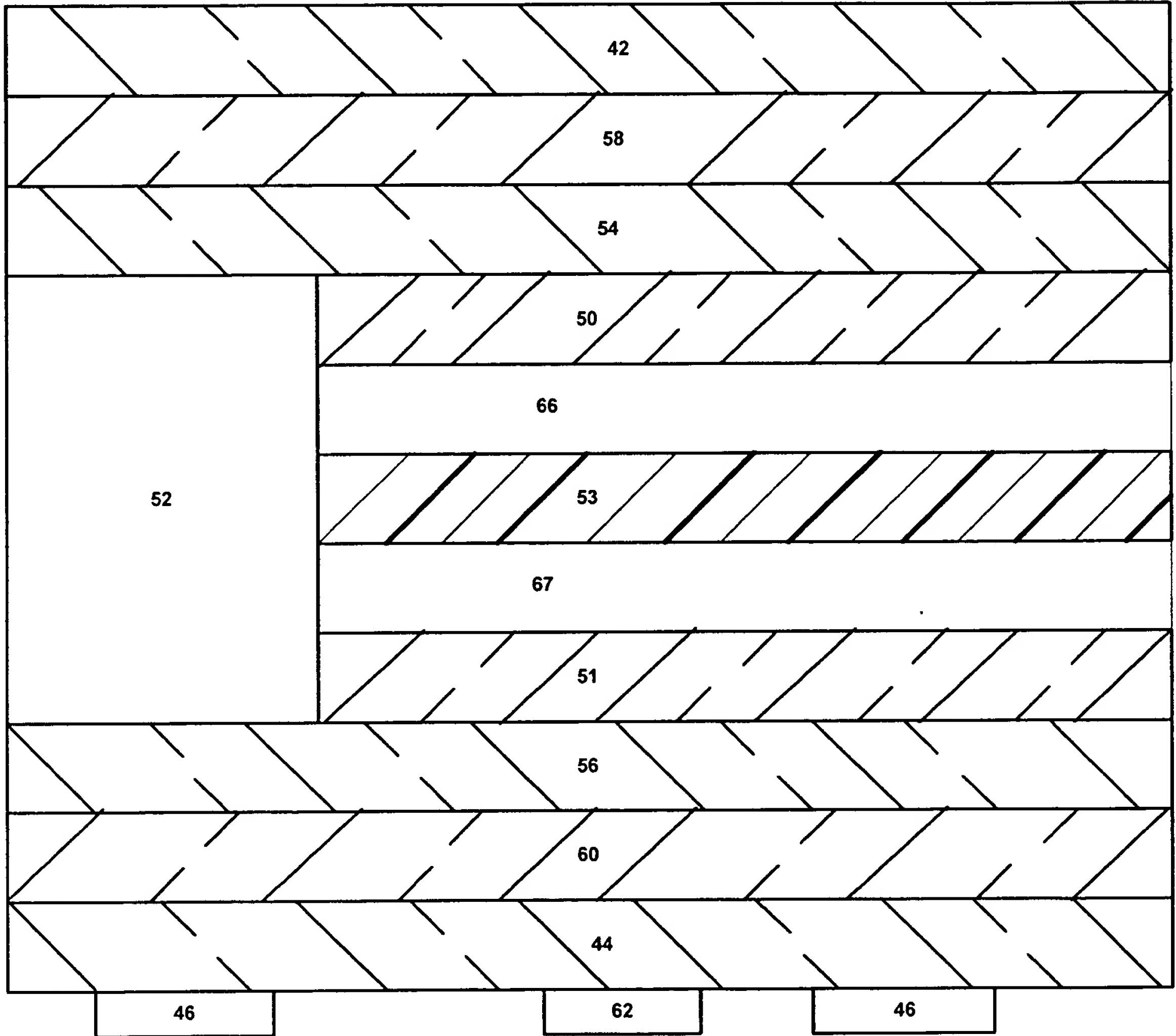
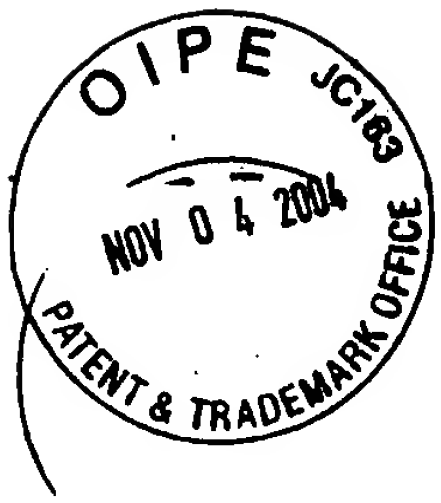


FIGURE 4D

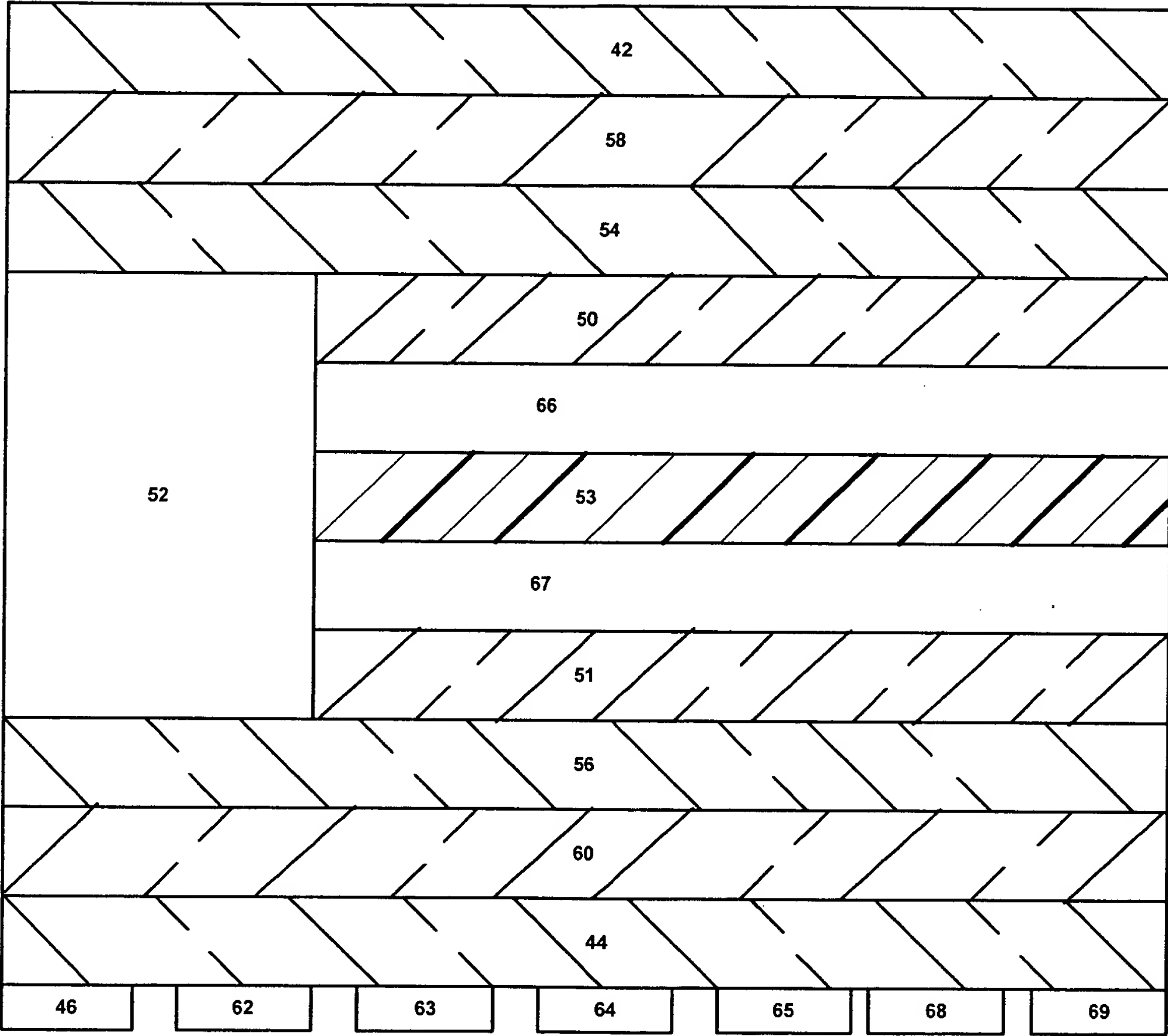


FIGURE 4E

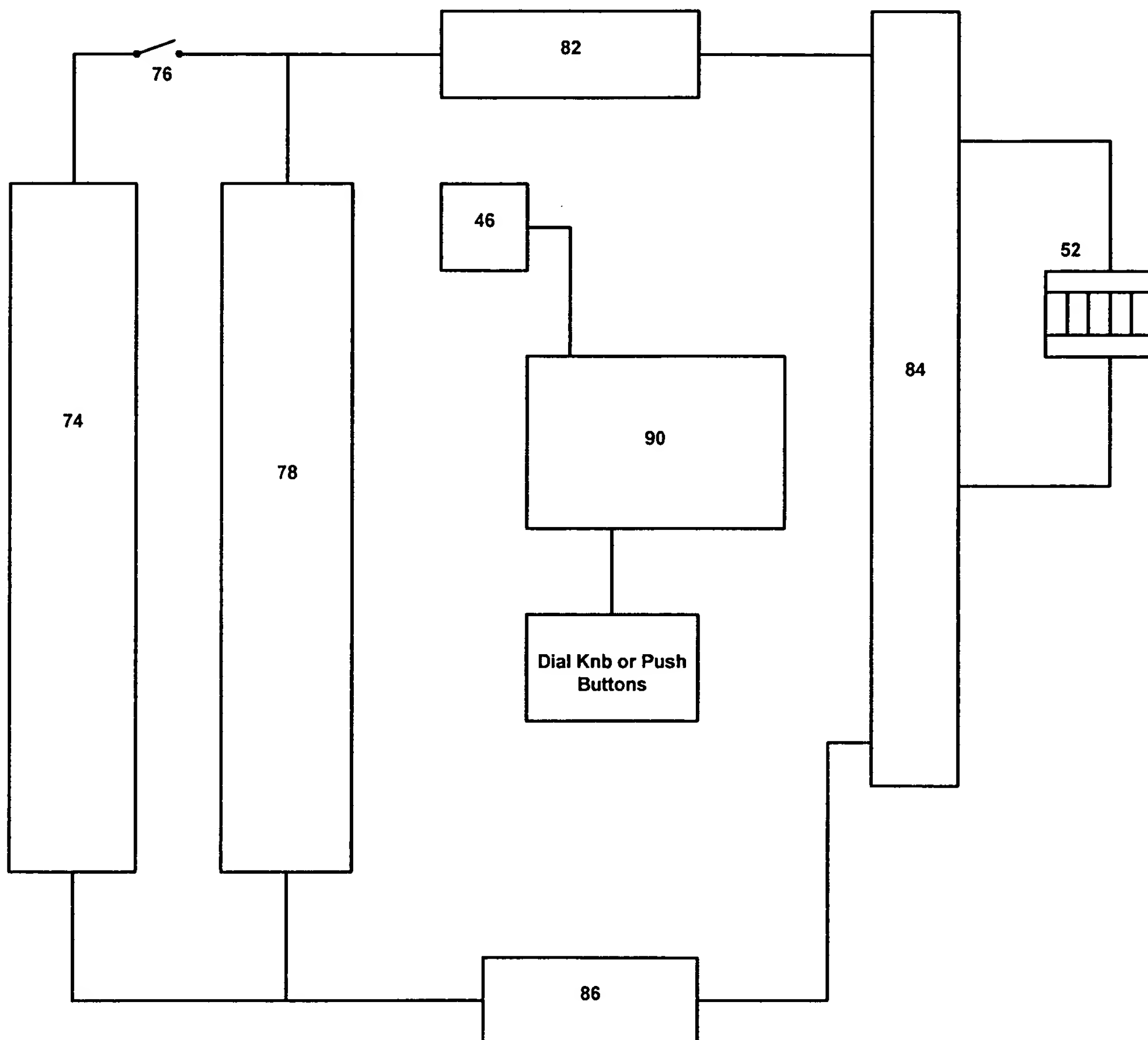


FIGURE 5

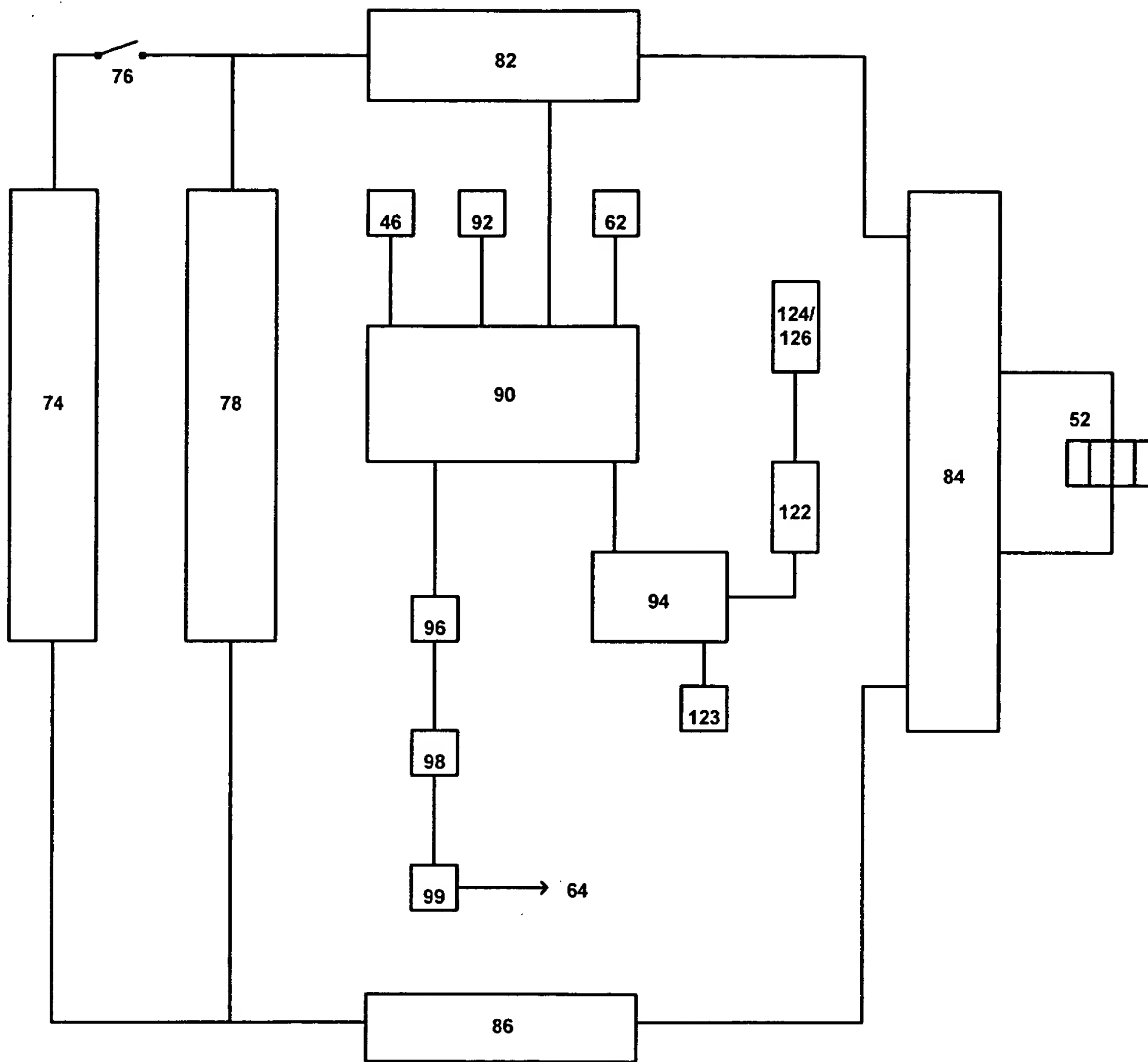


FIGURE 6

The diagram illustrates a system architecture with the following components and connections:

- Central Component (90):** A large rectangular block in the center.
- Input/Output Ports (Top):** Five small boxes labeled 46, 62, 64, 65, and 68 are connected to the top of component 90.
- Control/Status Unit (82):** A box at the top center, connected to component 90 and a horizontal line at the top.
- Memory/Storage (74, 78):** Two vertical rectangular blocks on the left side, connected to the top and bottom horizontal lines.
- Processing/Control Unit (86):** A box at the bottom center, connected to the bottom horizontal line.
- Peripheral Unit (84):** A vertical rectangular block on the right side, connected to the top and bottom horizontal lines.
- Data/Control Flow (Right Side):** A vertical chain of boxes: 124/126, 122, 94, and 123, connected to component 90.
- Subsystems (Bottom):**
 - Component 96 is connected to 90, leading to 98 and then 99.
 - Component 130 is connected to 90, leading to 132 and then 134.
 - Component 99 has an output labeled 64.
 - Component 134 has two outputs labeled 136 and 138.
- Other Components:** A box labeled 52 is connected to the right side of the system, and a box labeled 84 is connected to the right side of the system.

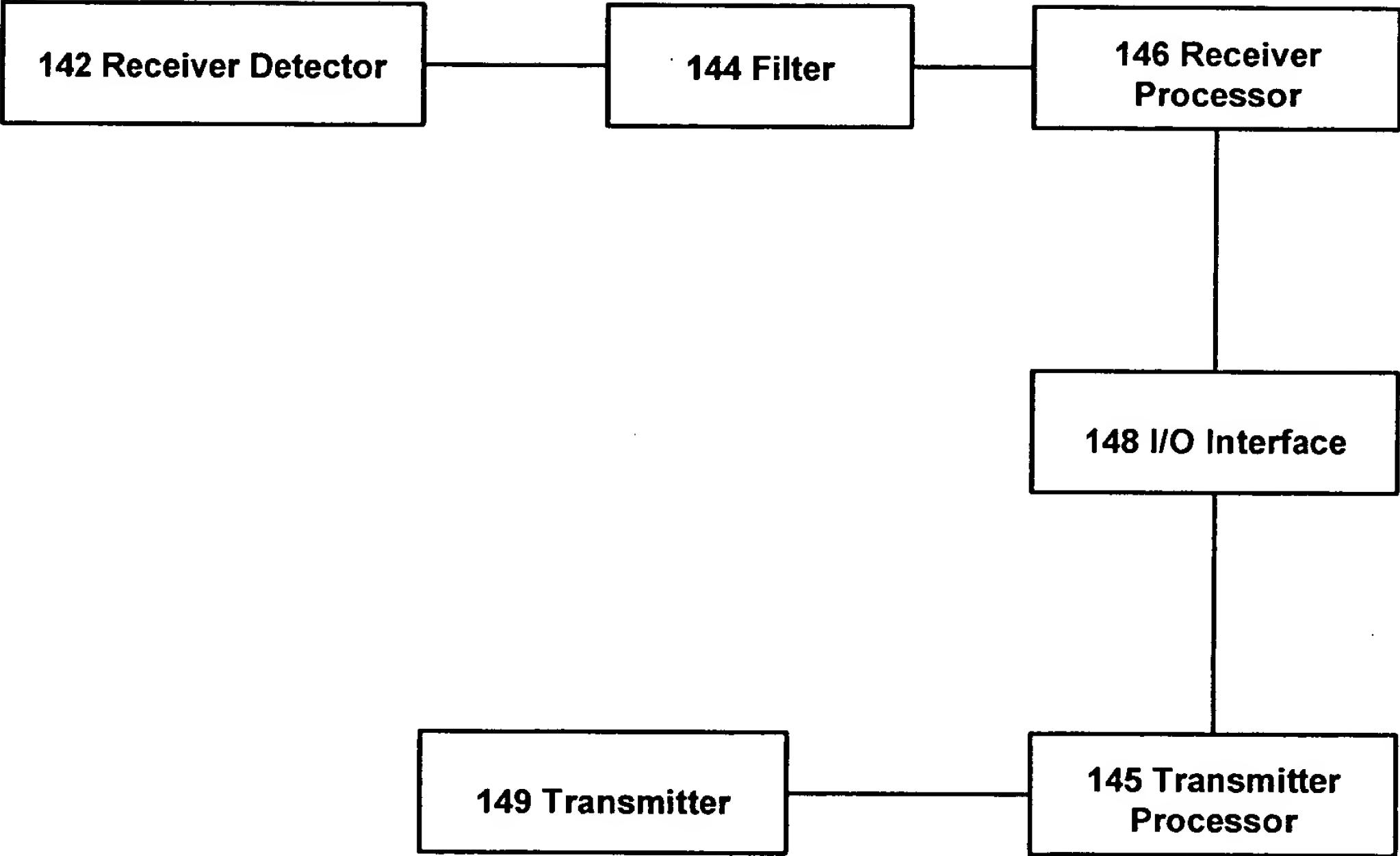


FIGURE 8

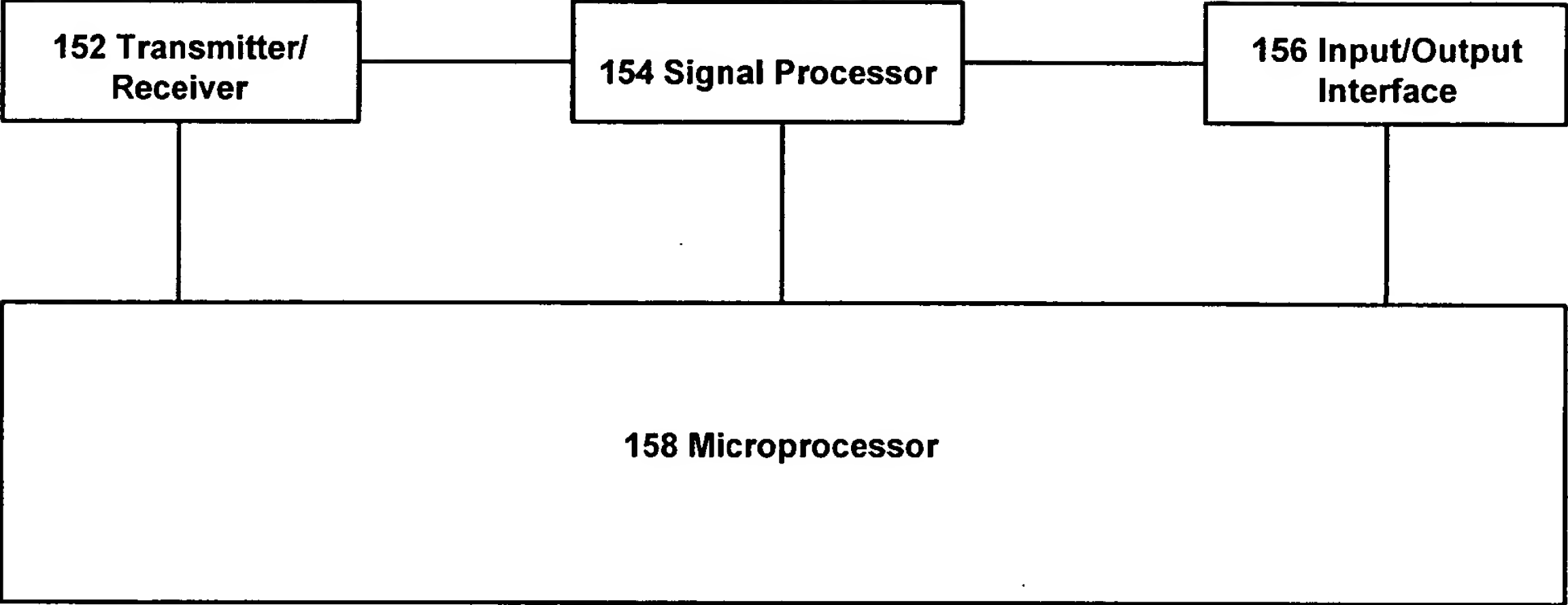


FIGURE 9

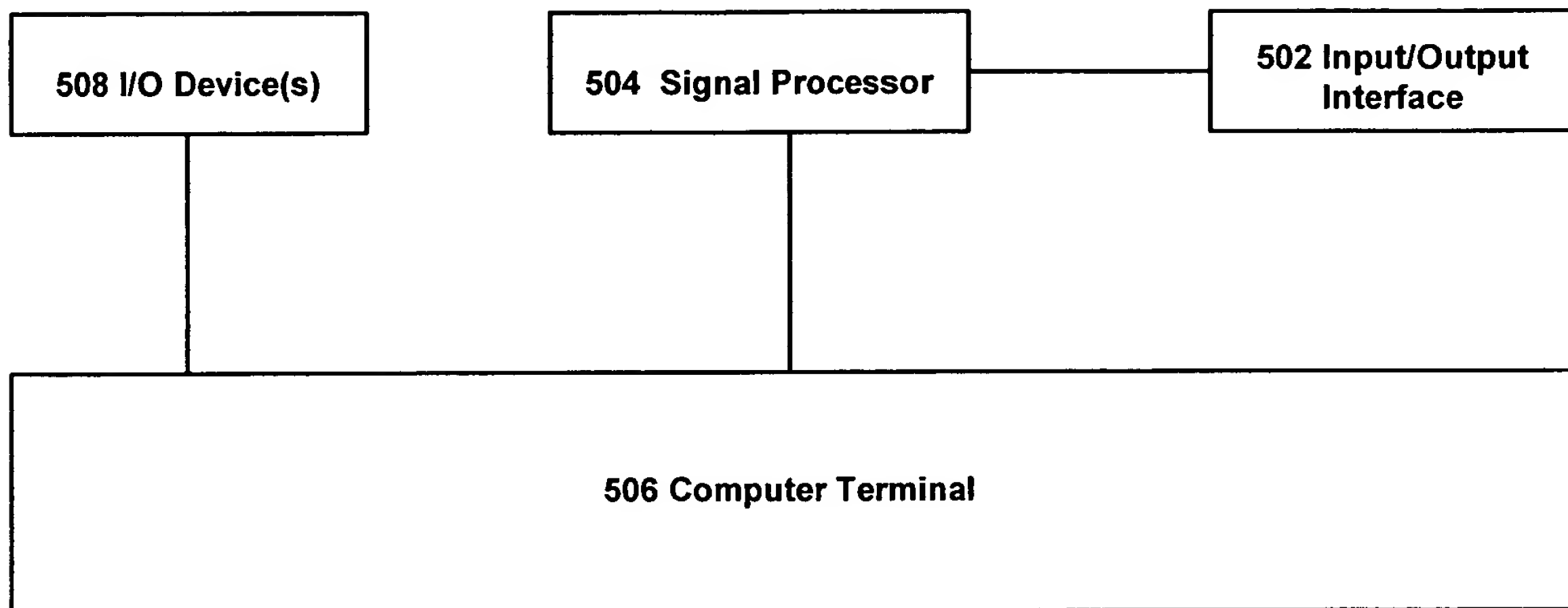


FIGURE 10

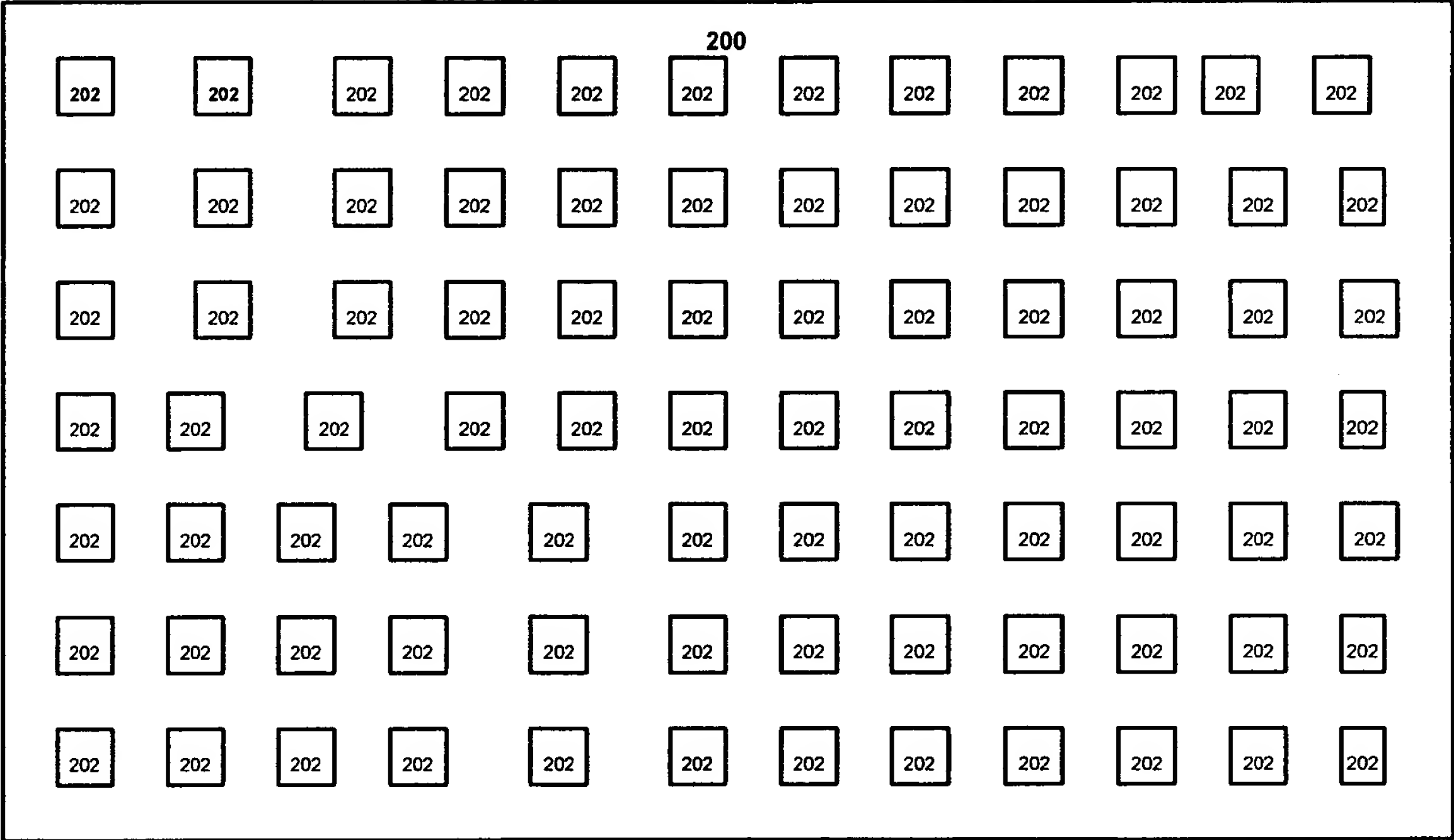


FIGURE 11

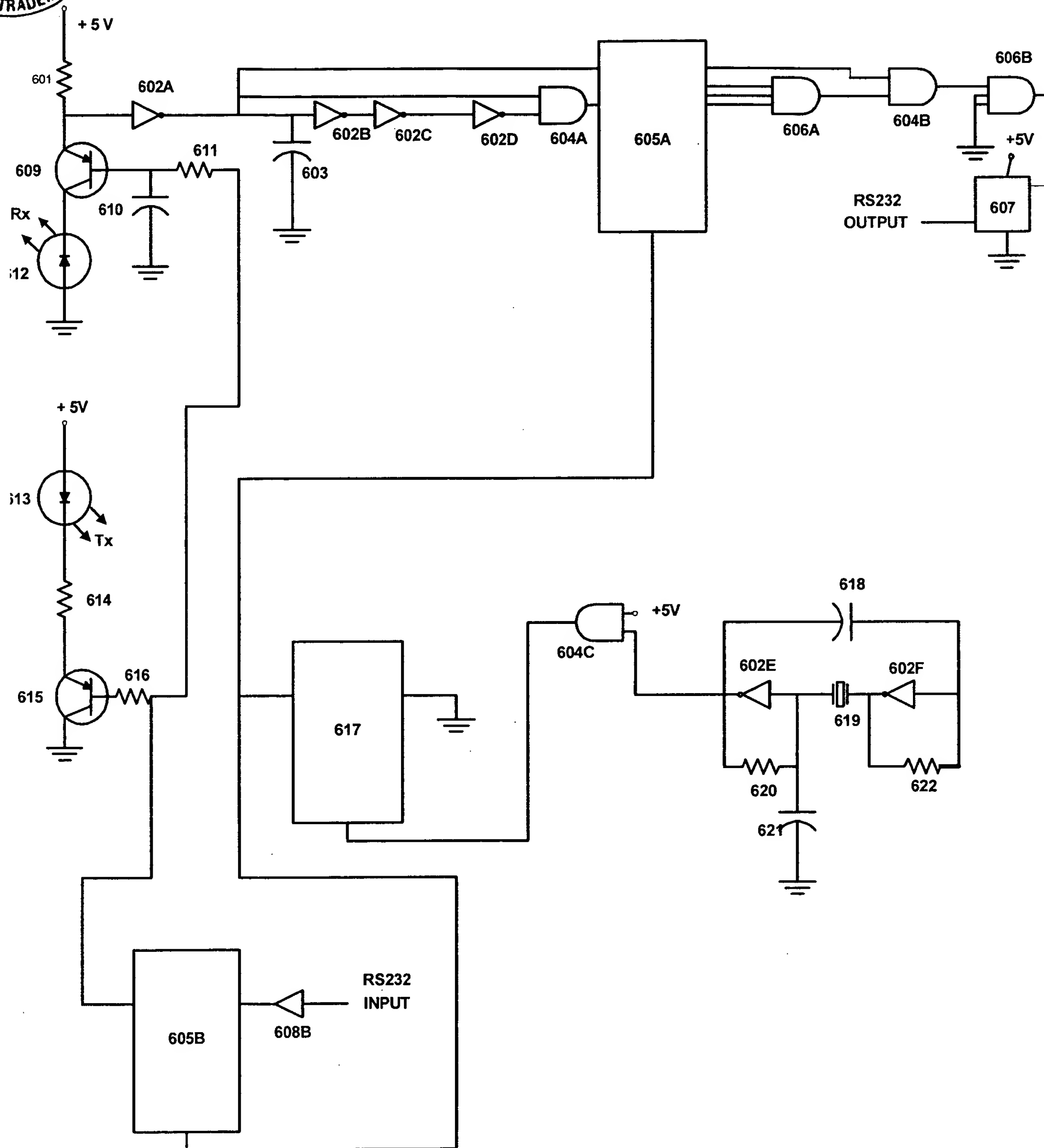


FIGURE 12

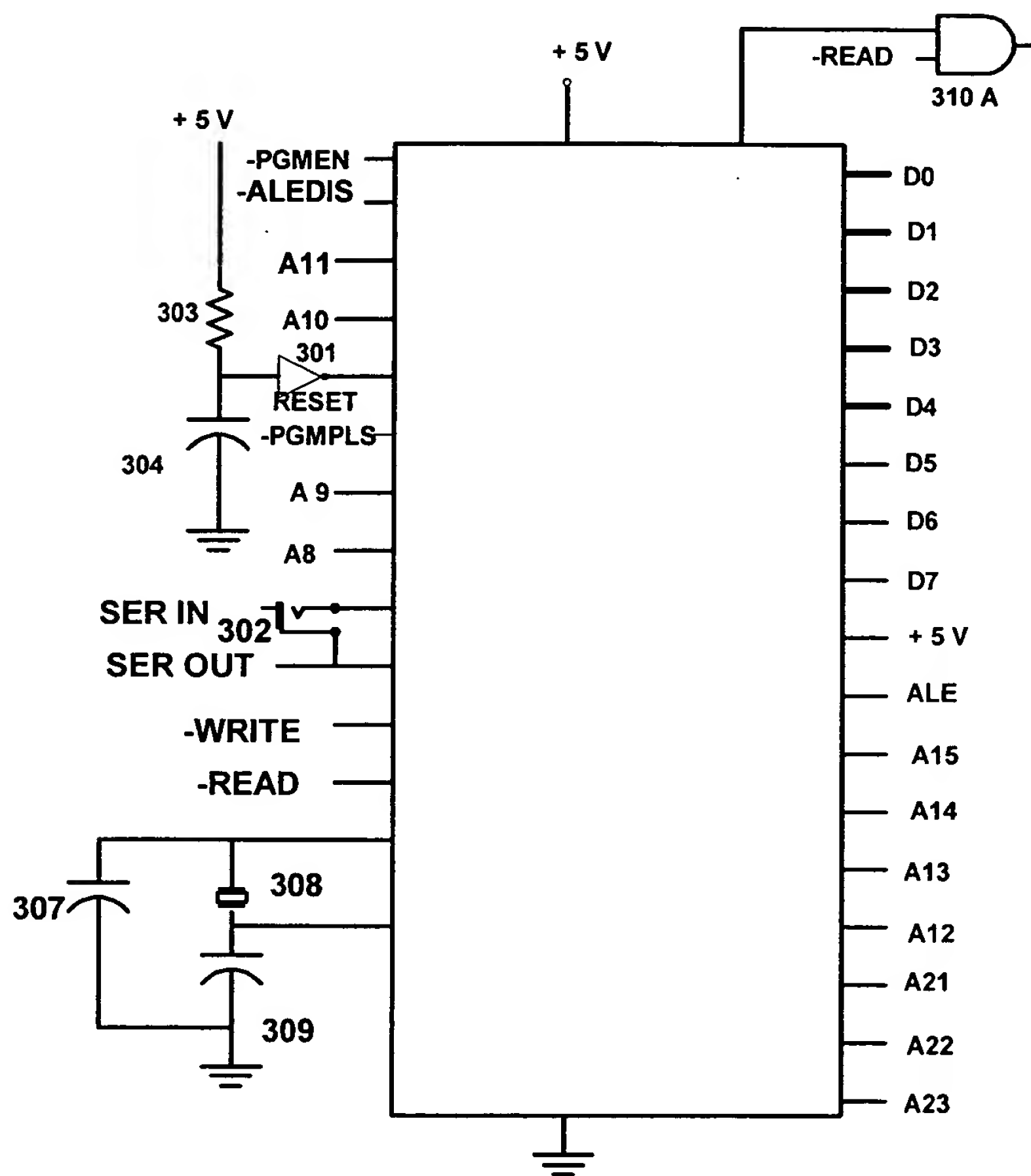


FIGURE 13



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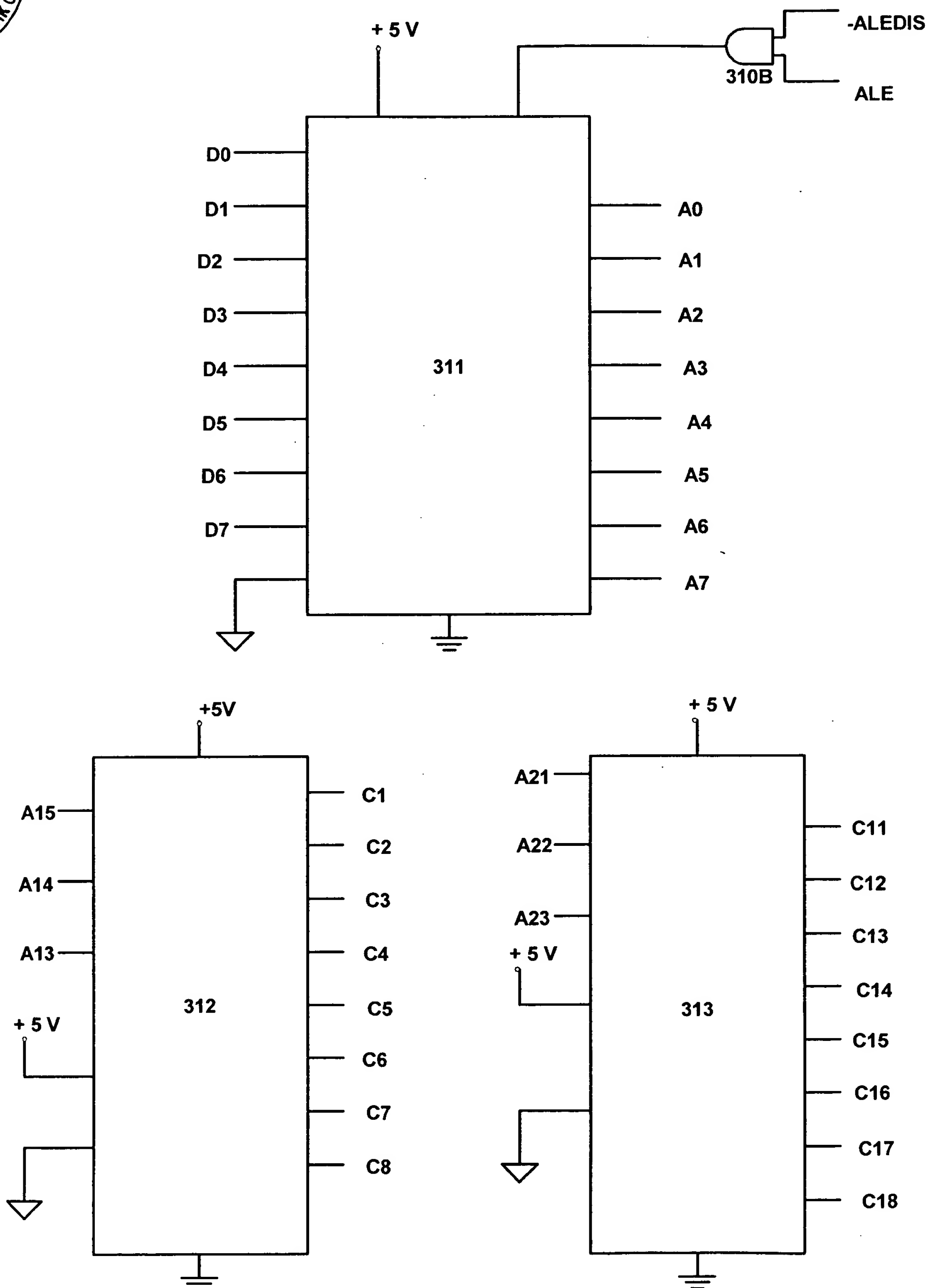


FIGURE 14

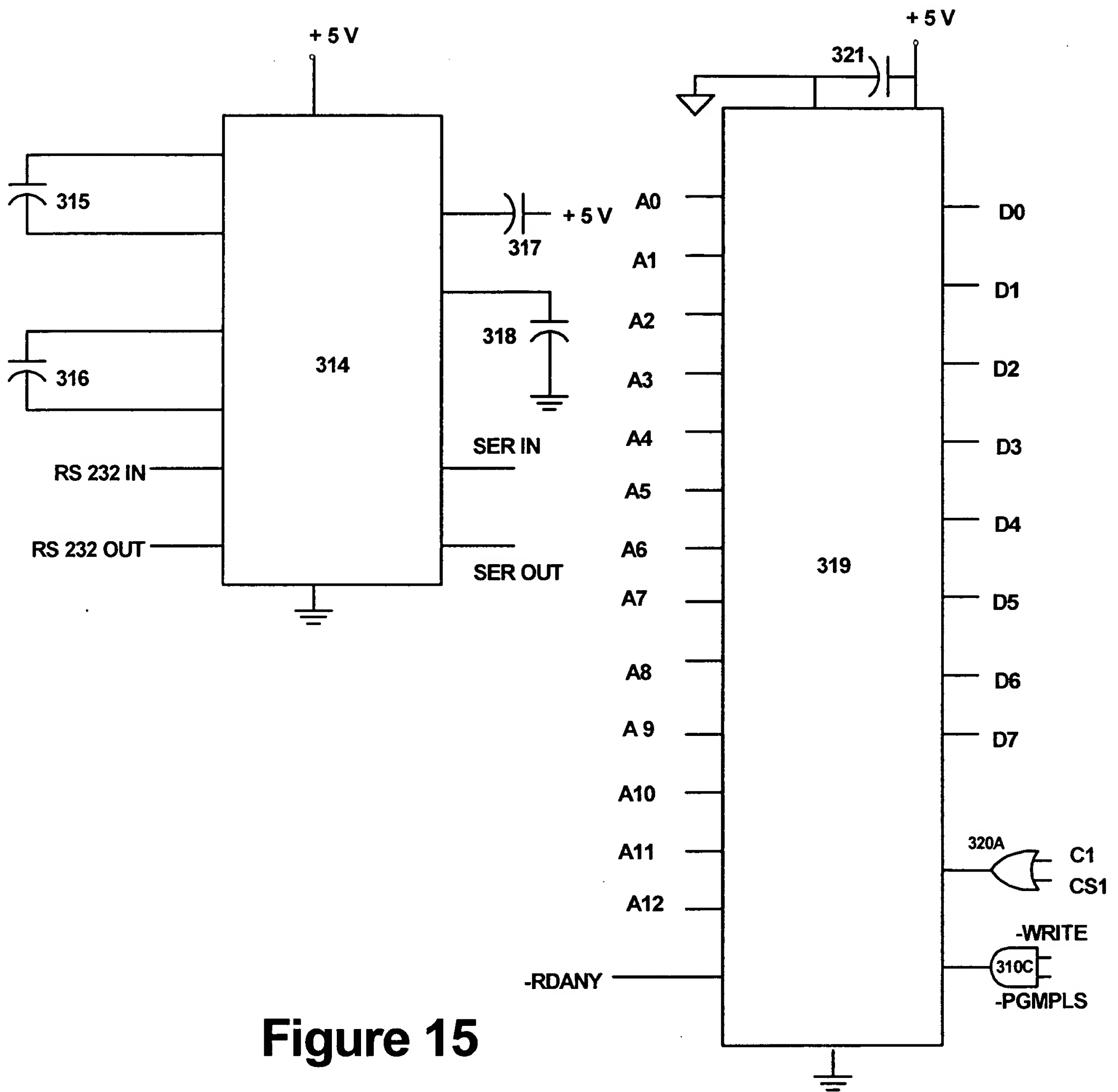


Figure 15

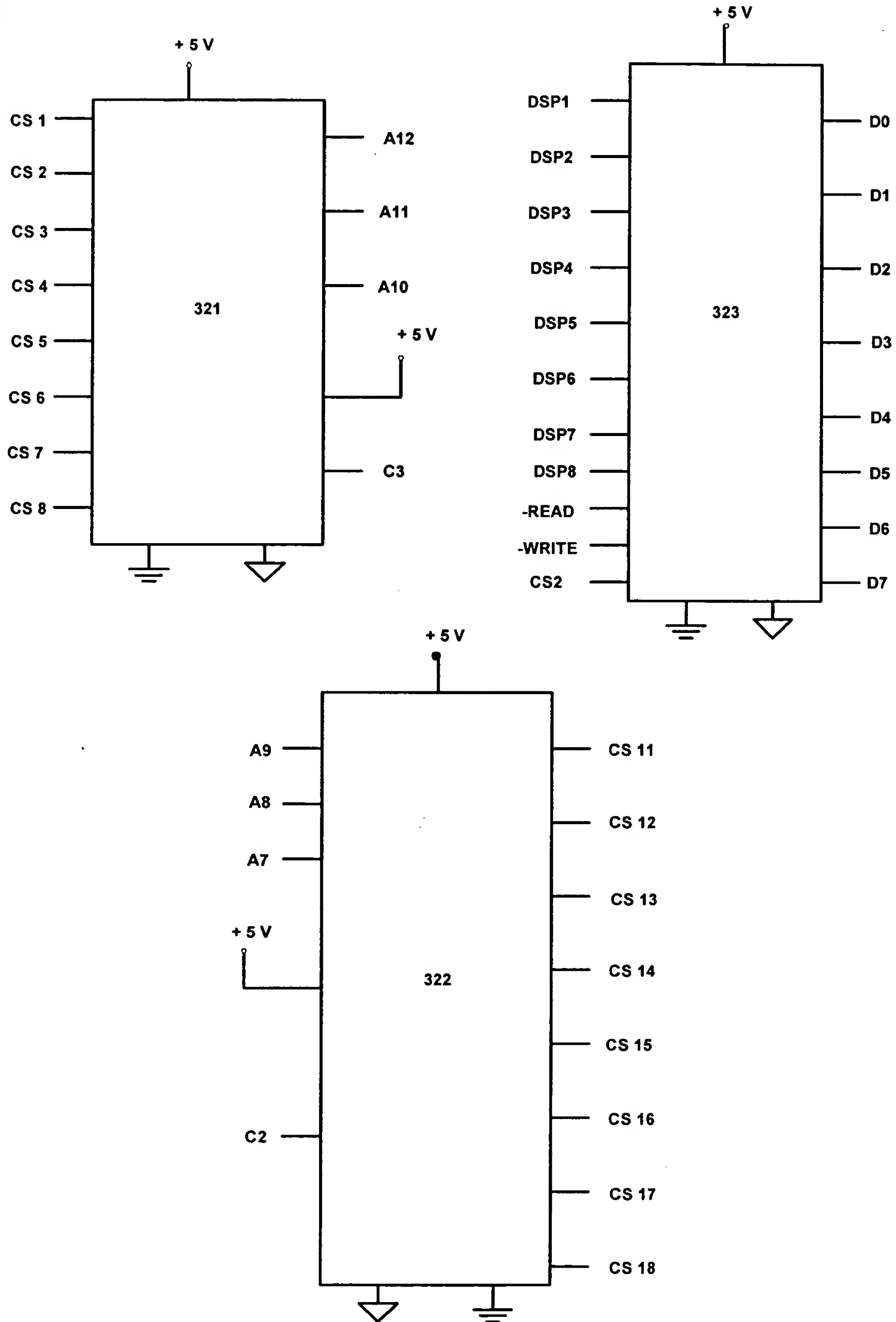


FIGURE 16

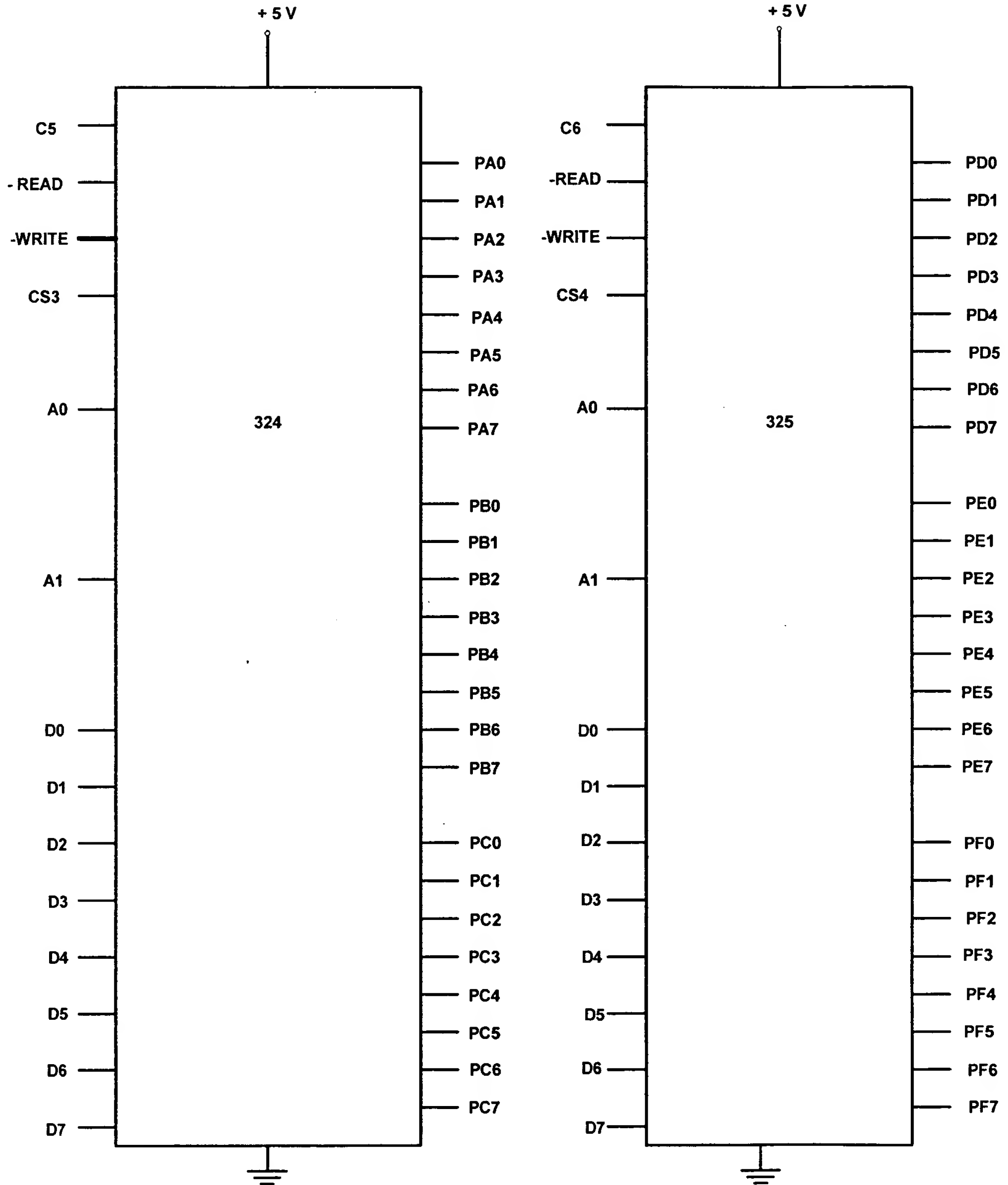


Figure 17

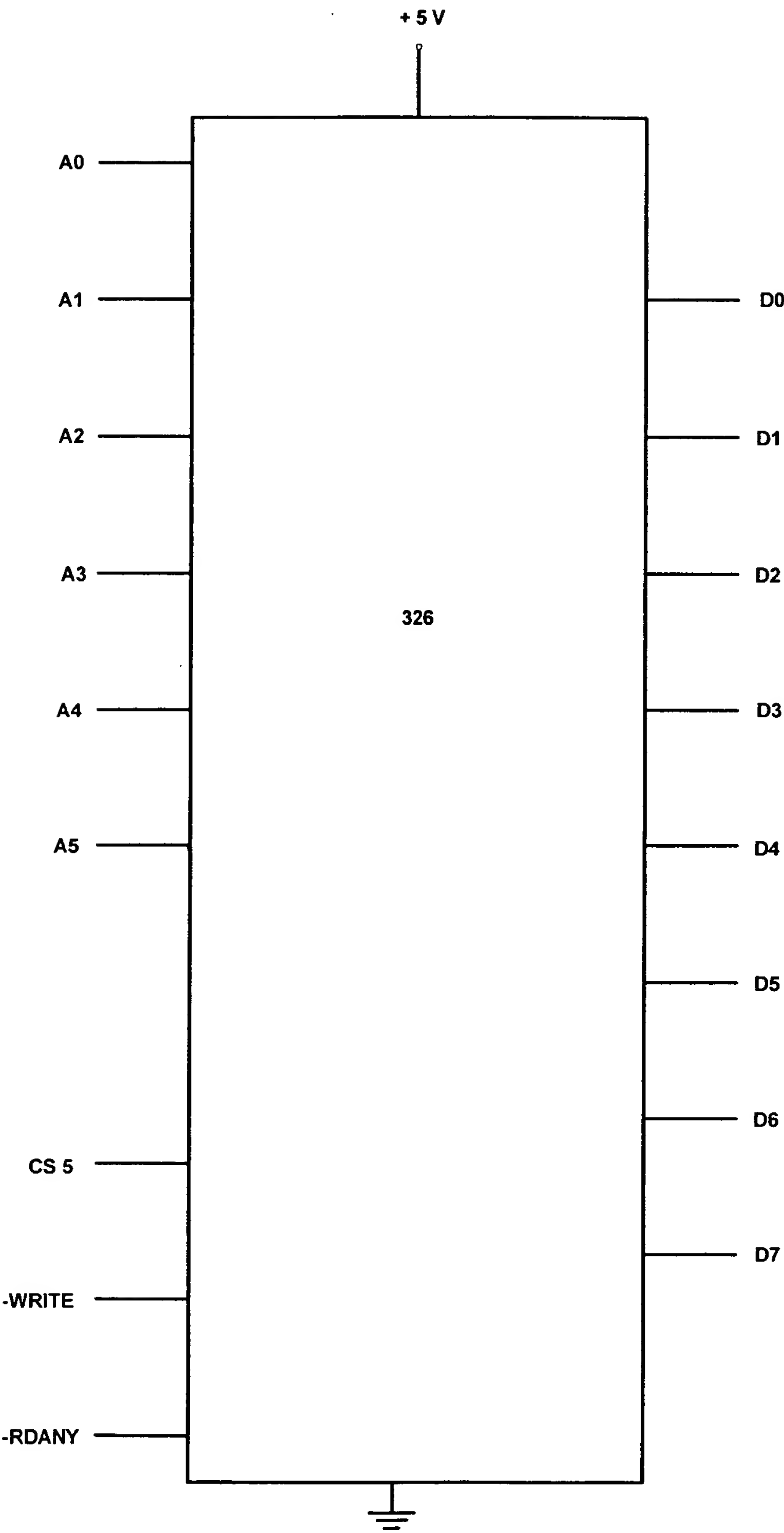


Figure 18

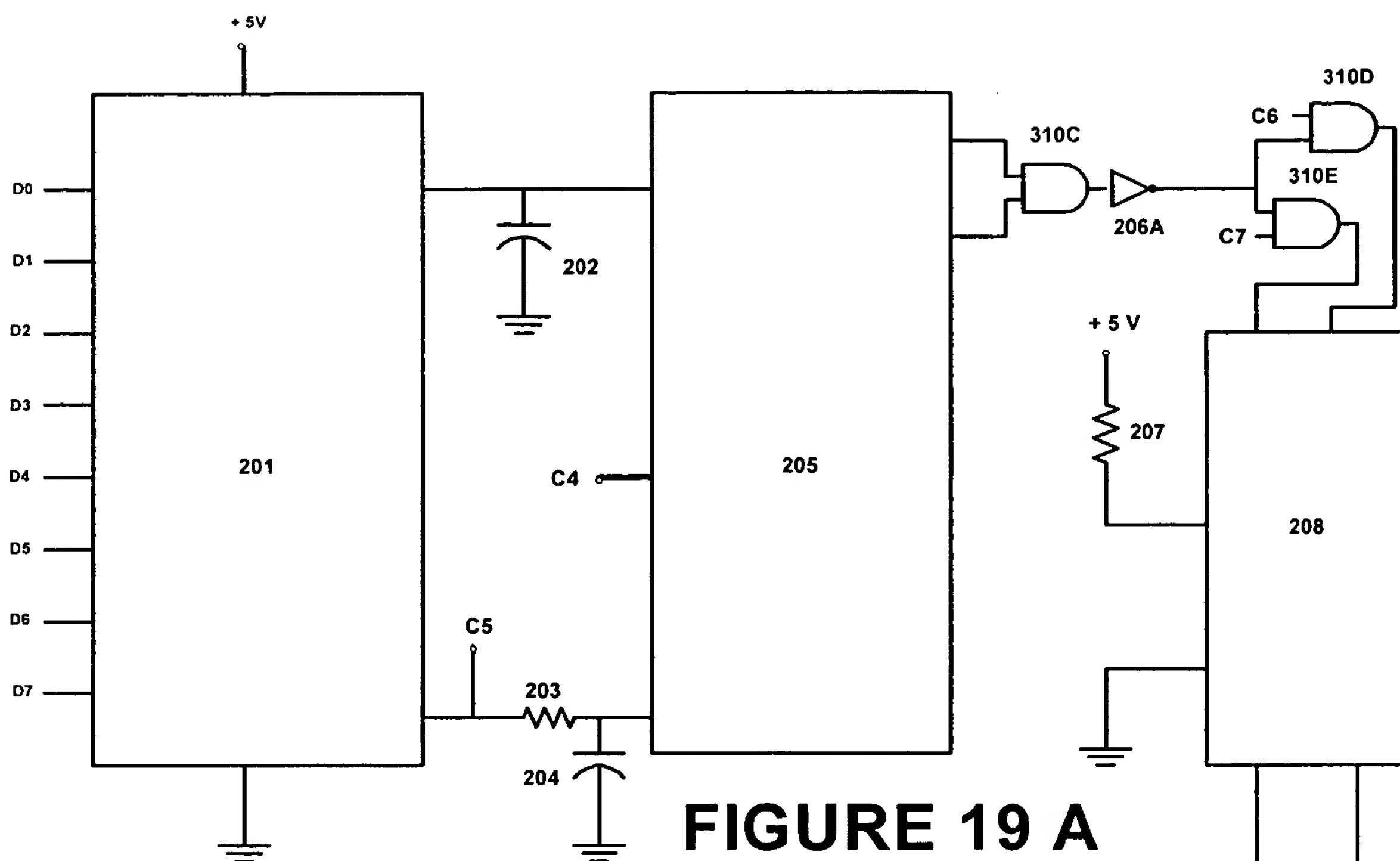


FIGURE 19 A

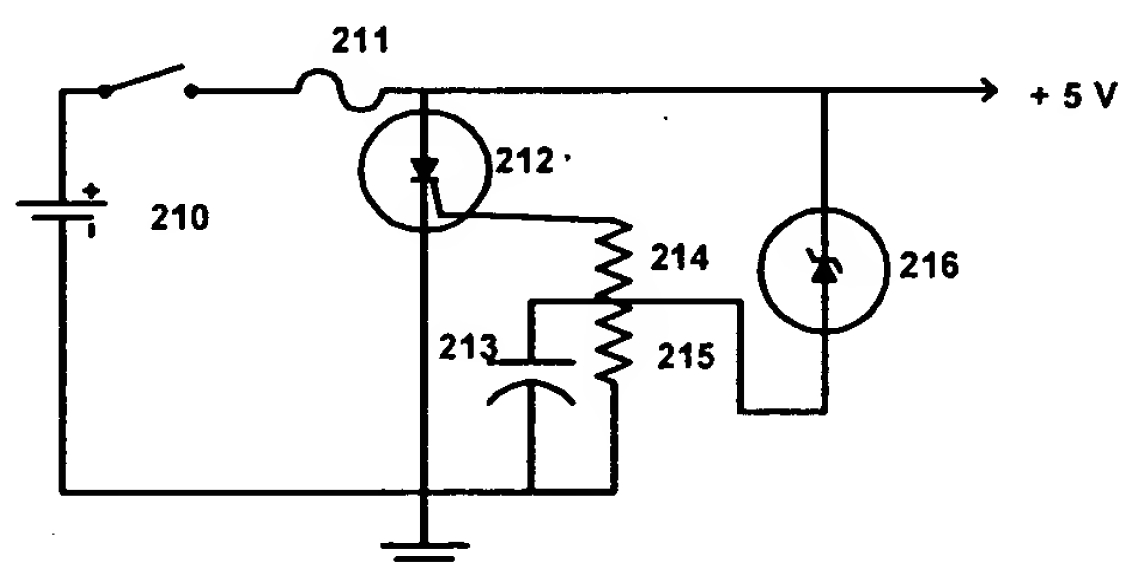


FIGURE 19 B

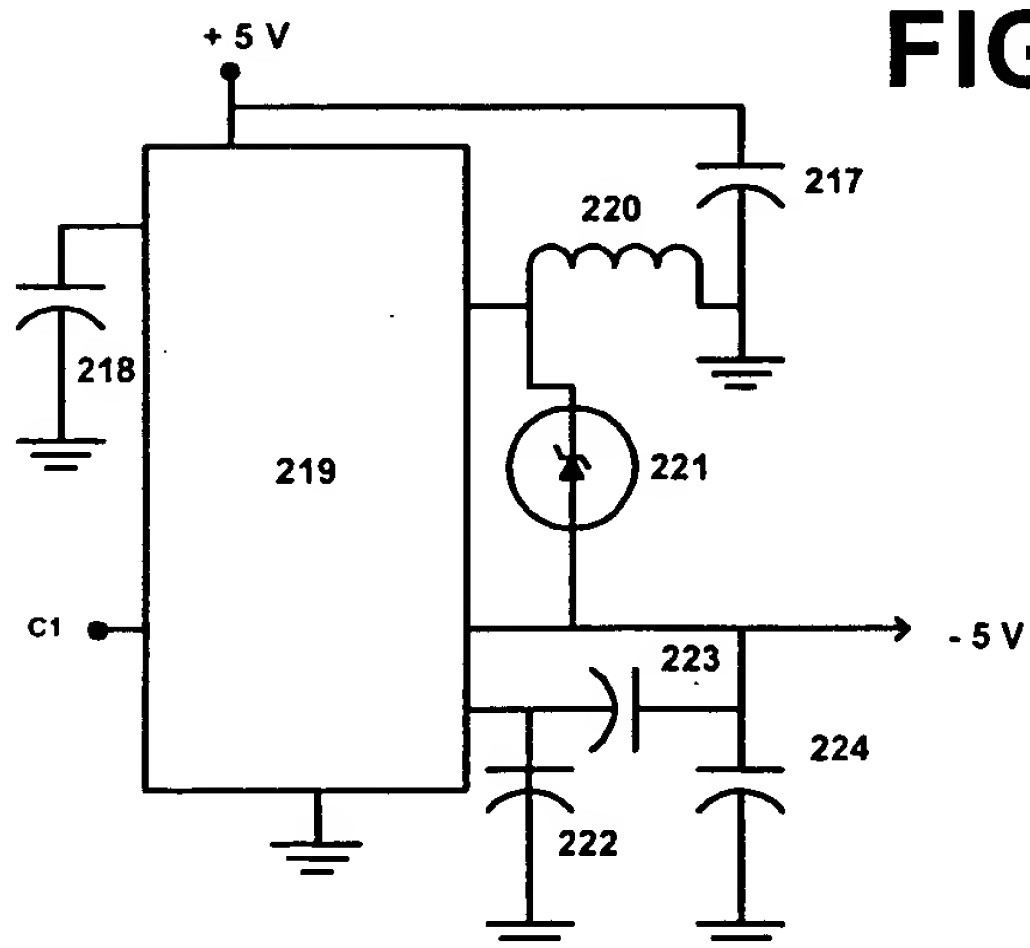


Figure 19 C

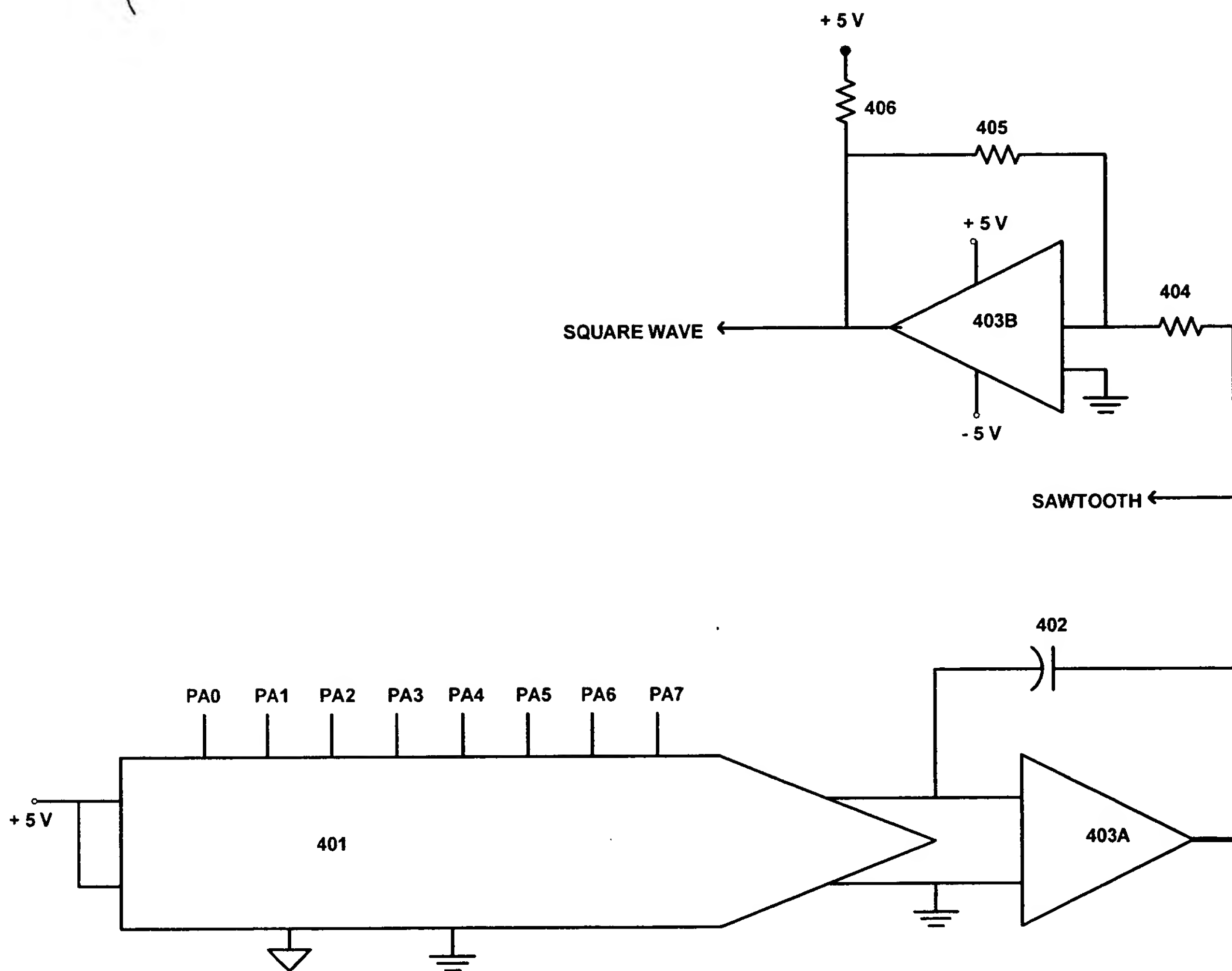


Figure 20

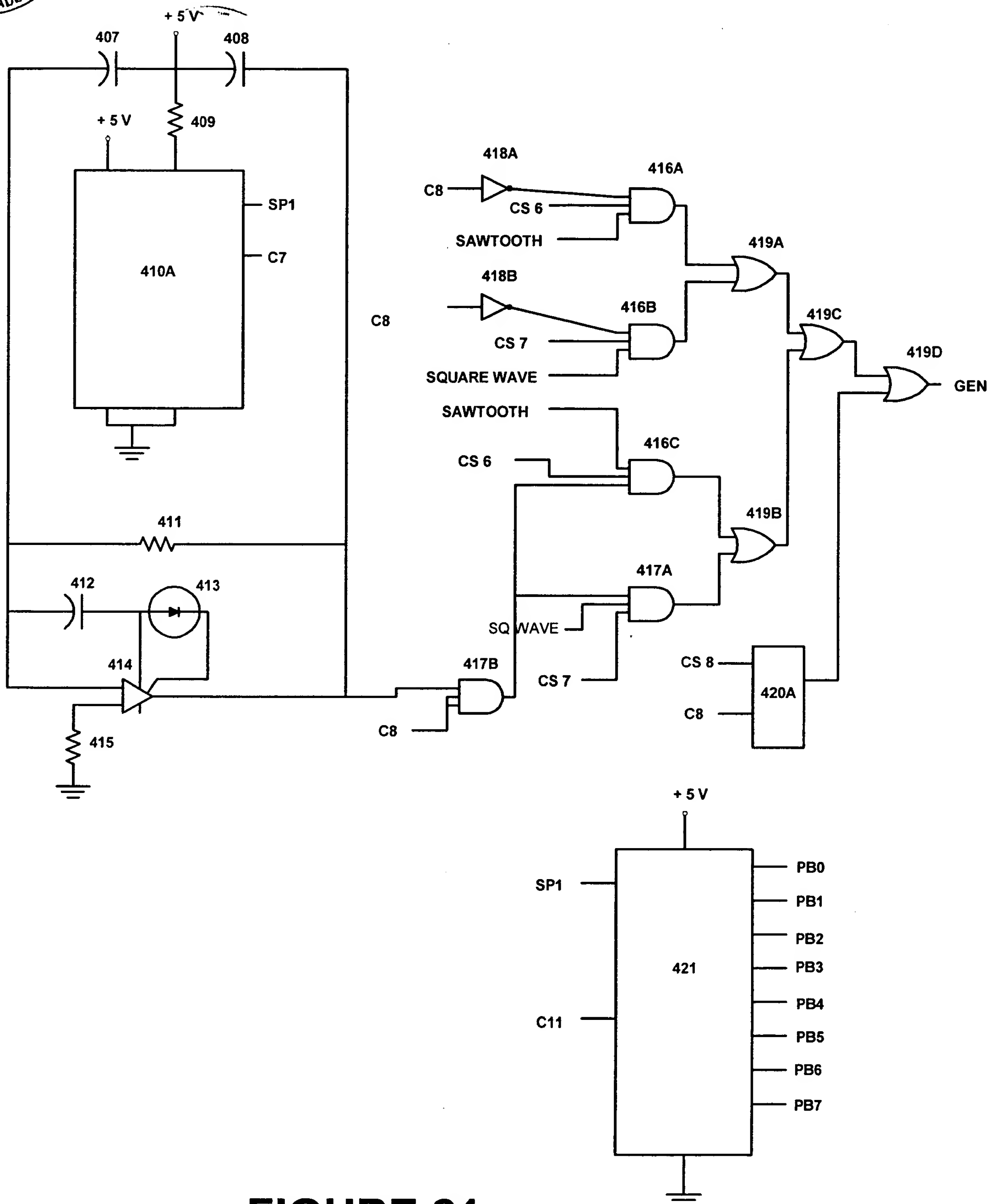


FIGURE 21

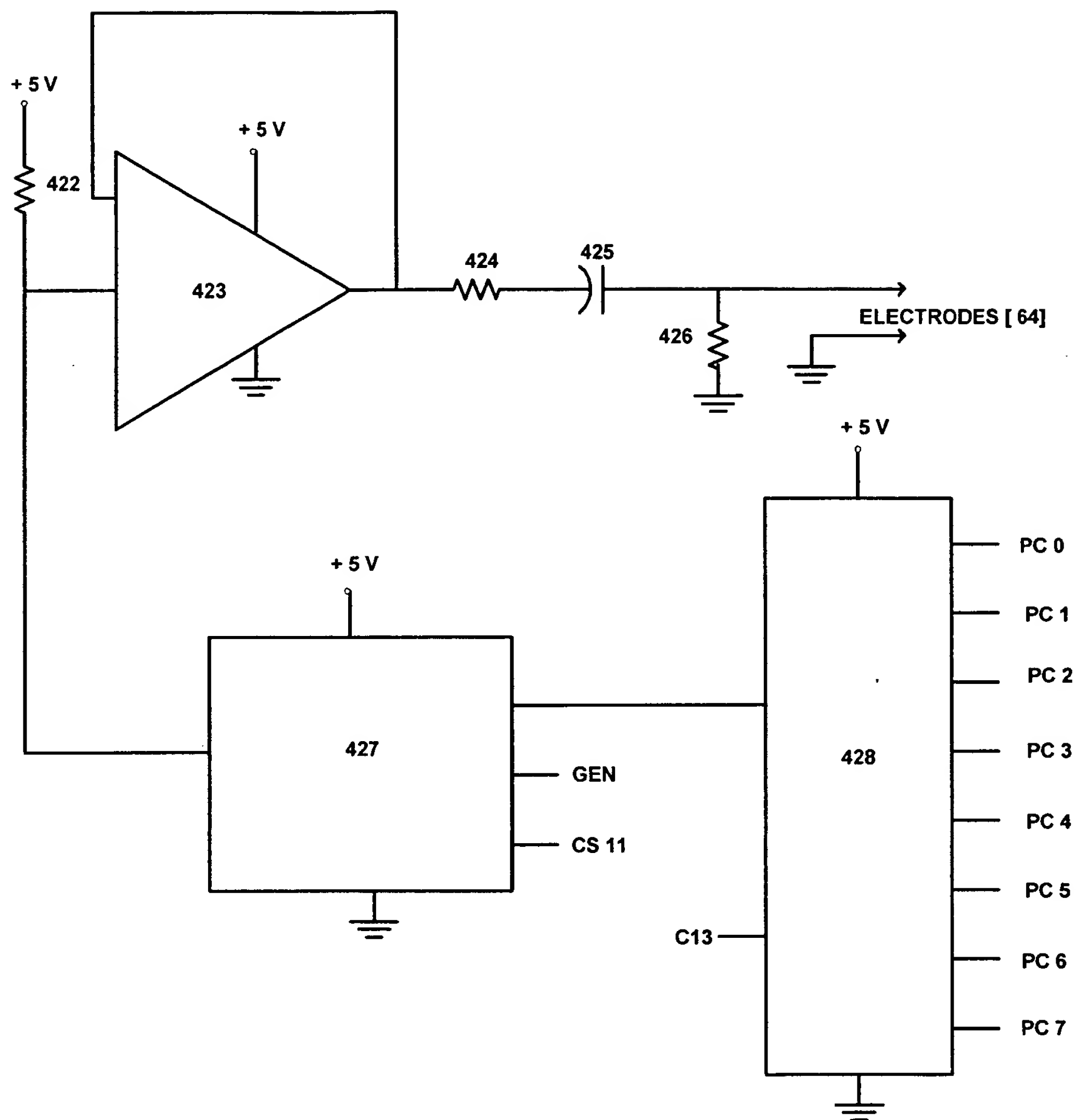


FIGURE 22

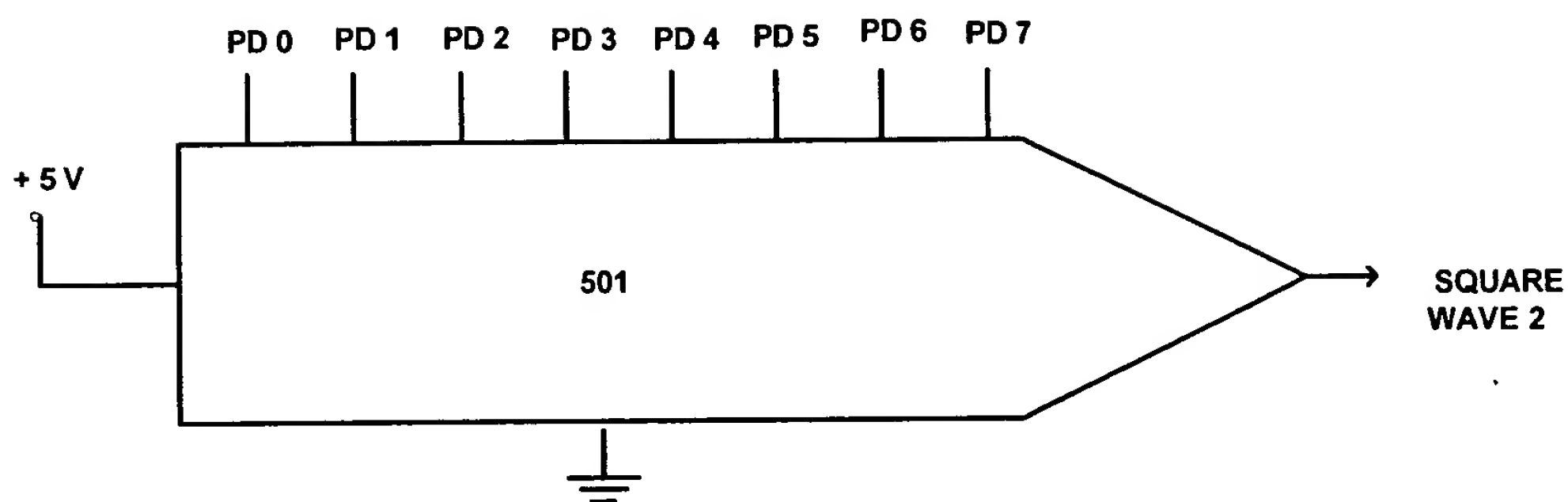


FIGURE 23

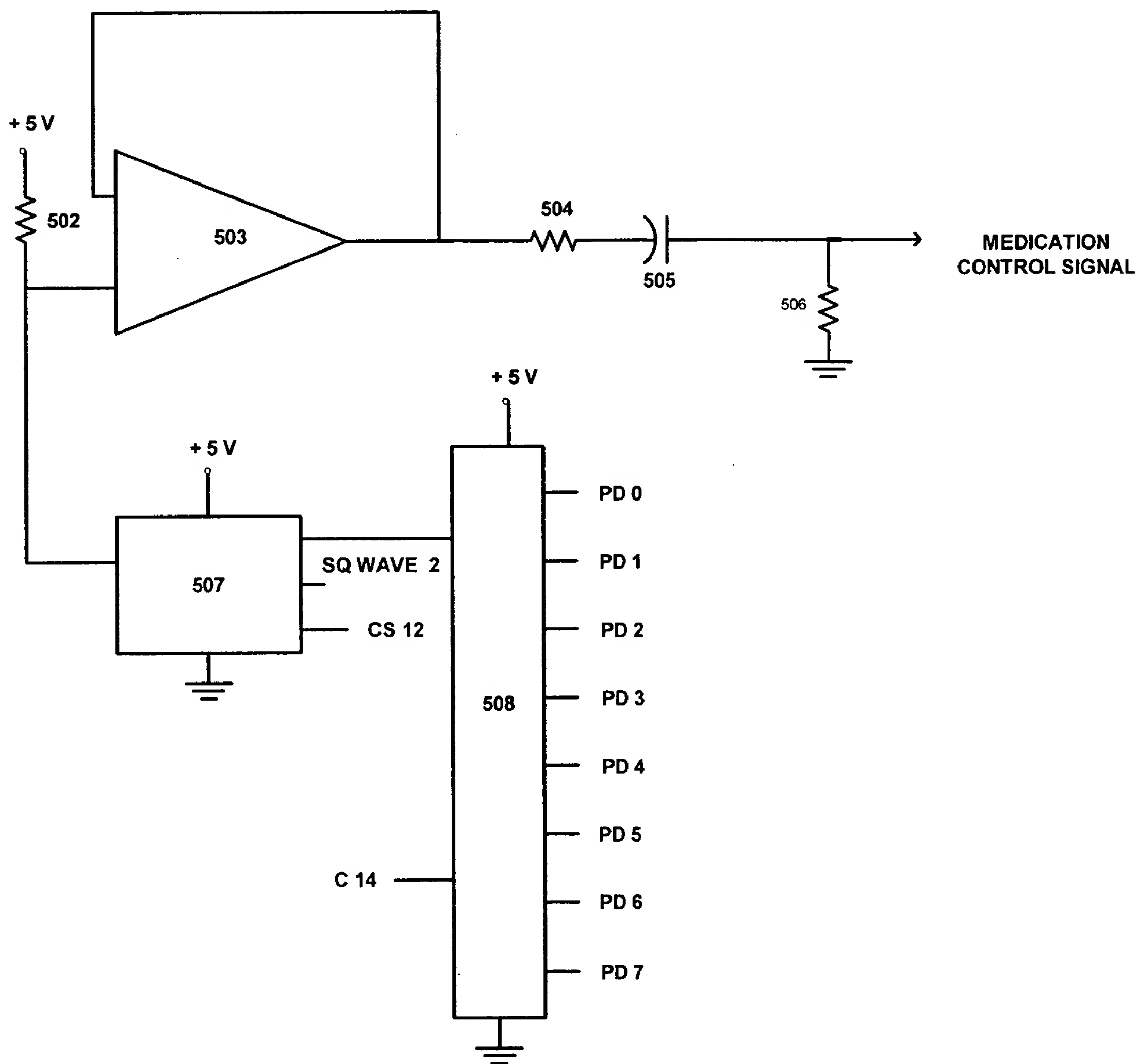
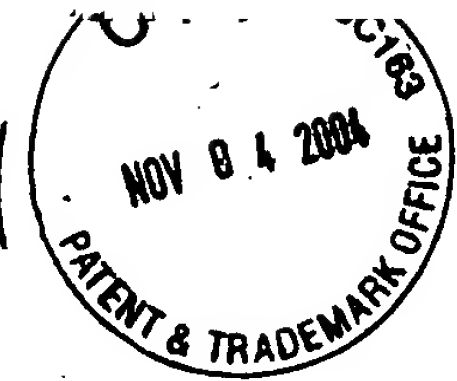


FIGURE 24



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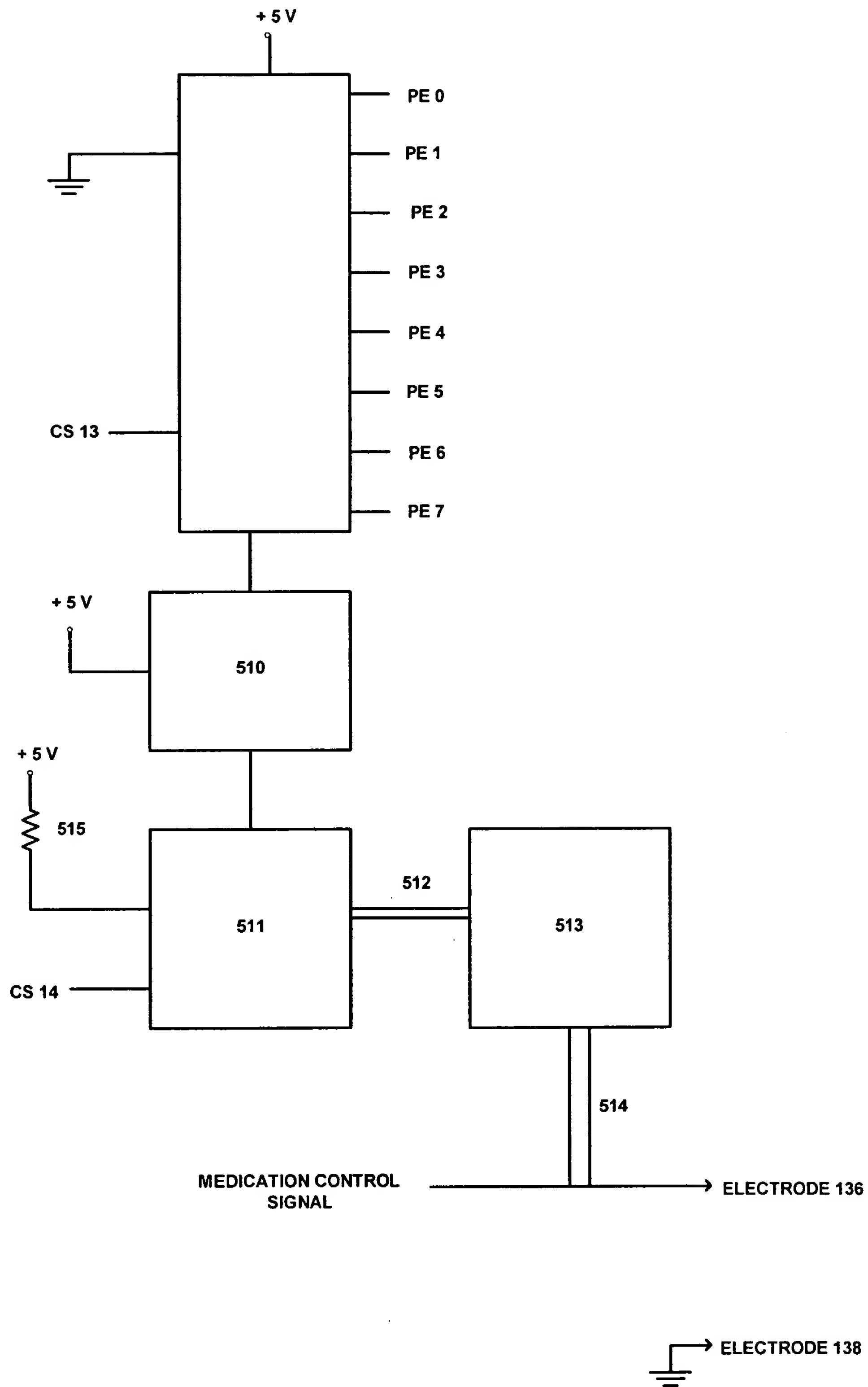


FIGURE 25

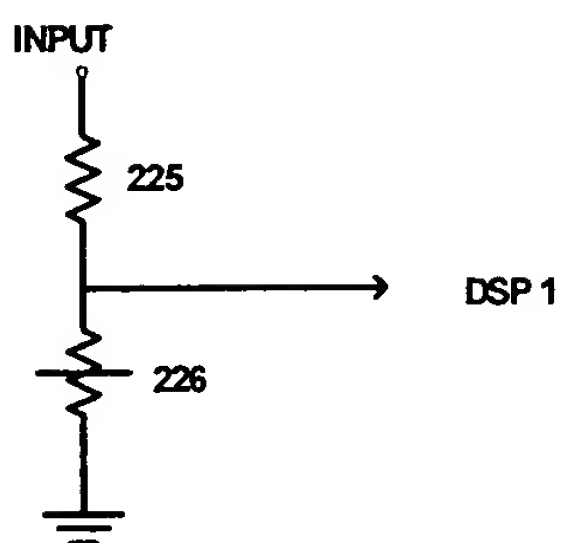


FIGURE 26 A

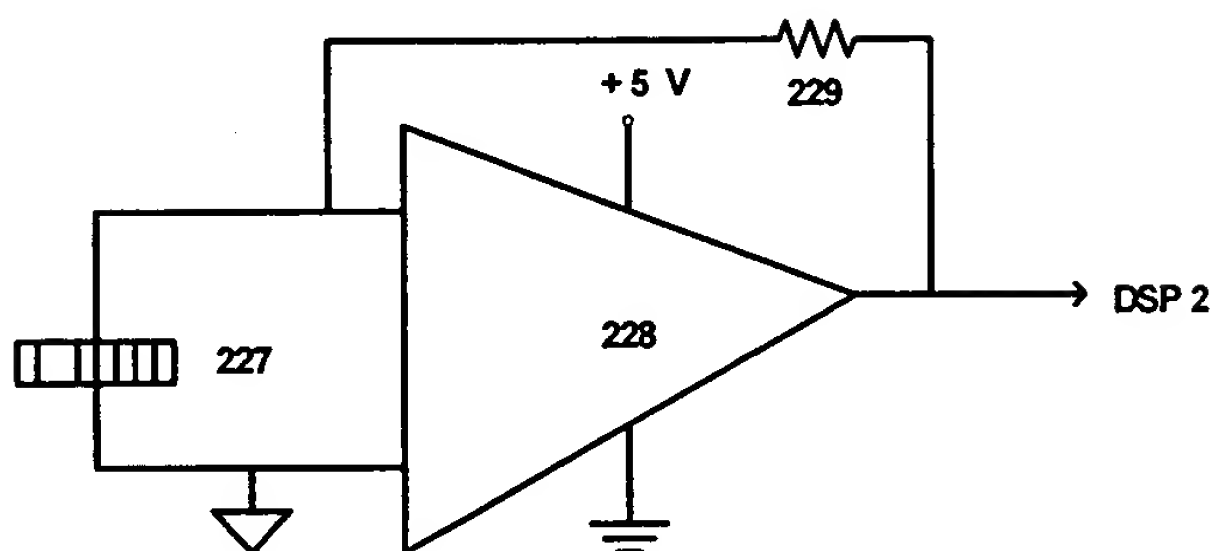


FIGURE 26 B

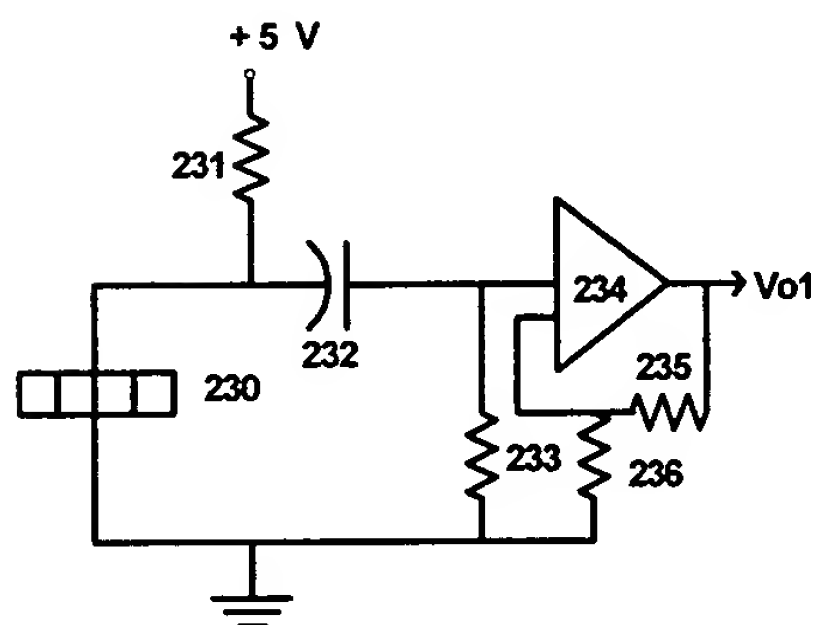


FIGURE 26 C

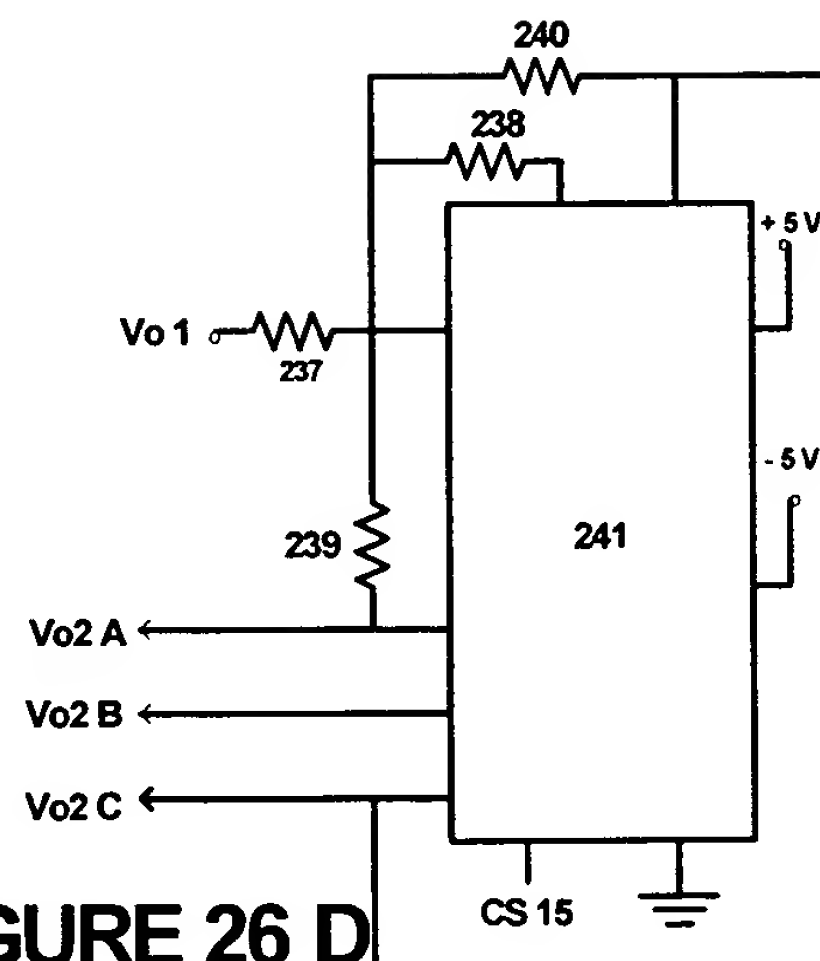


FIGURE 26 D

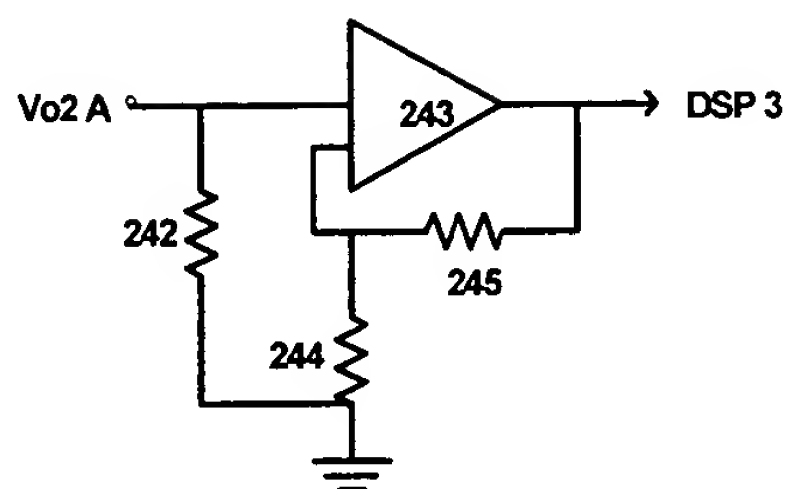


FIGURE 26 E

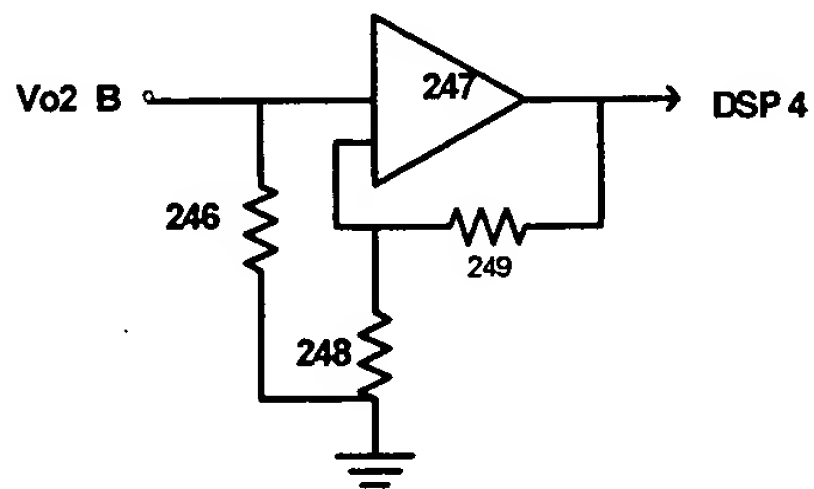


FIGURE 26 F

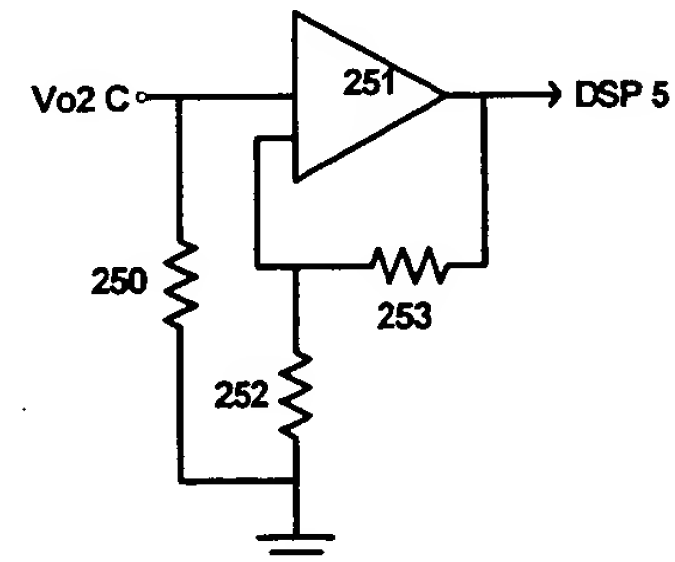


FIGURE 26 G

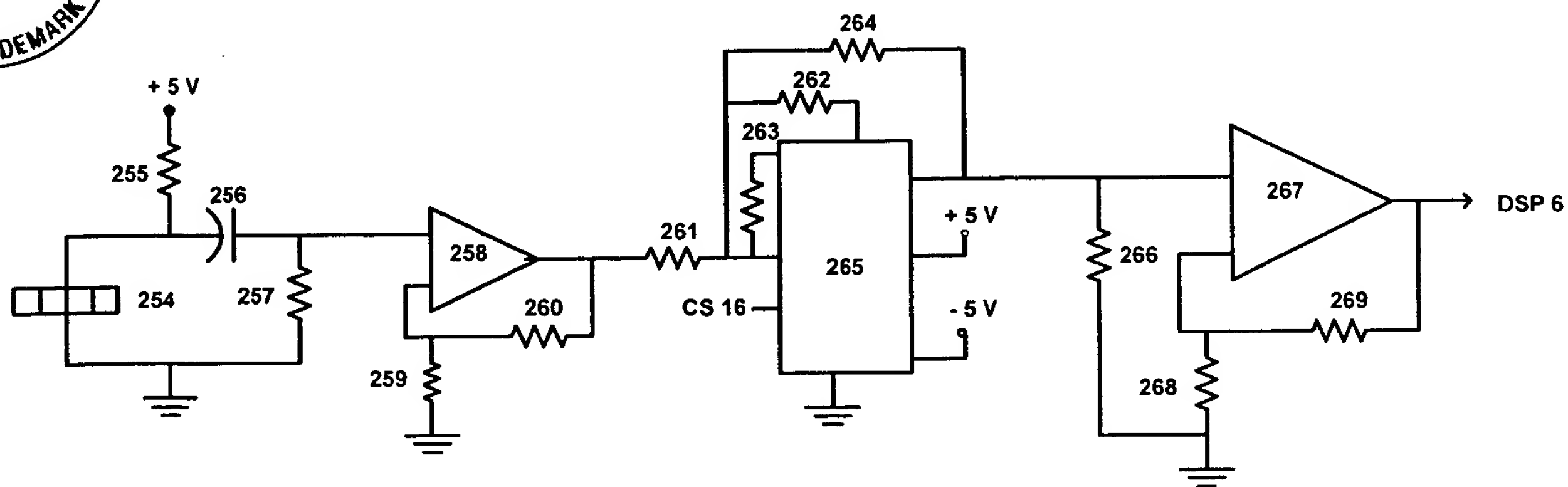


FIGURE 27 A

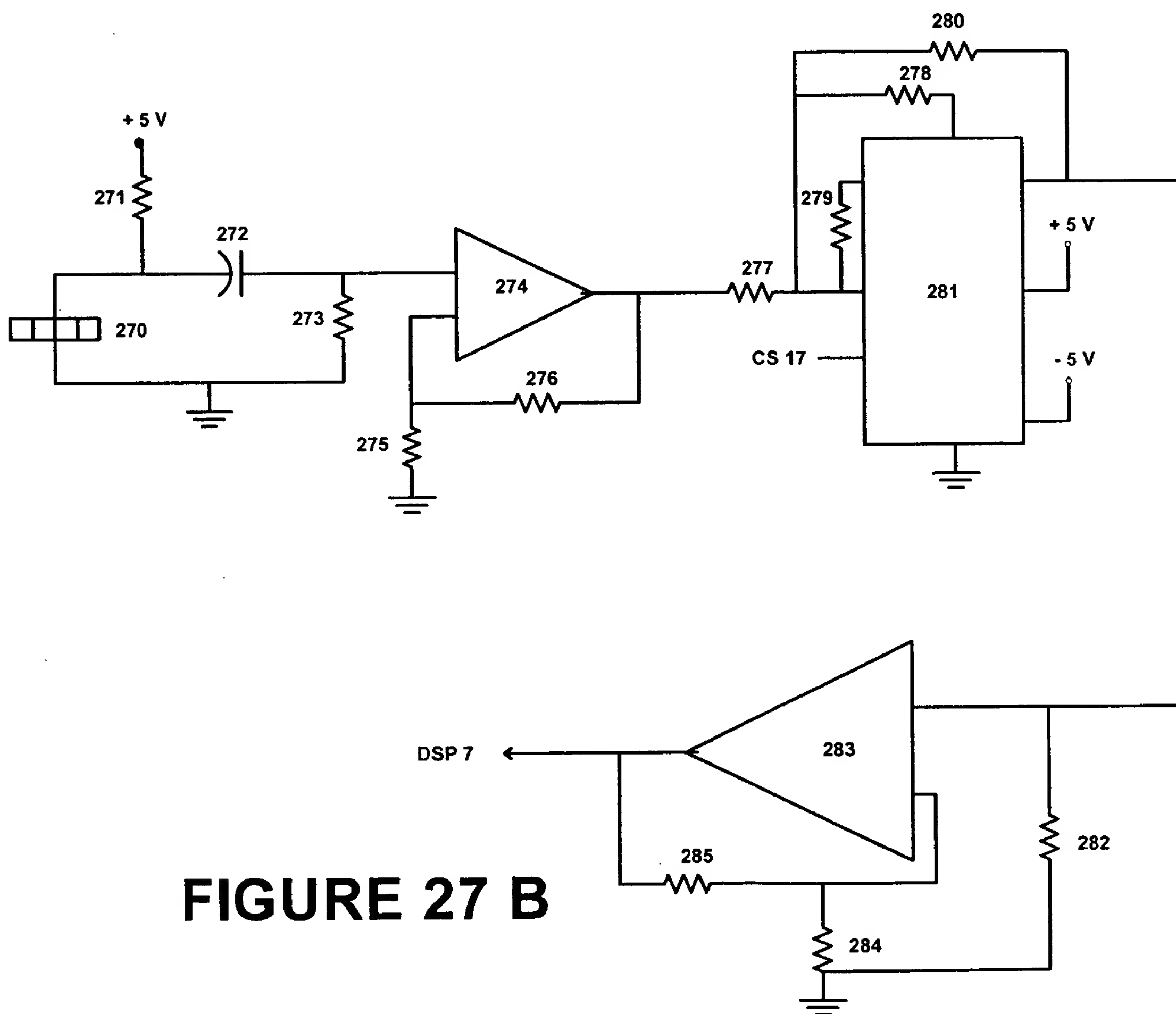


FIGURE 27 B